

▶ MITX-CFLO SERIES - QUICK INSTALLATION GUIDE

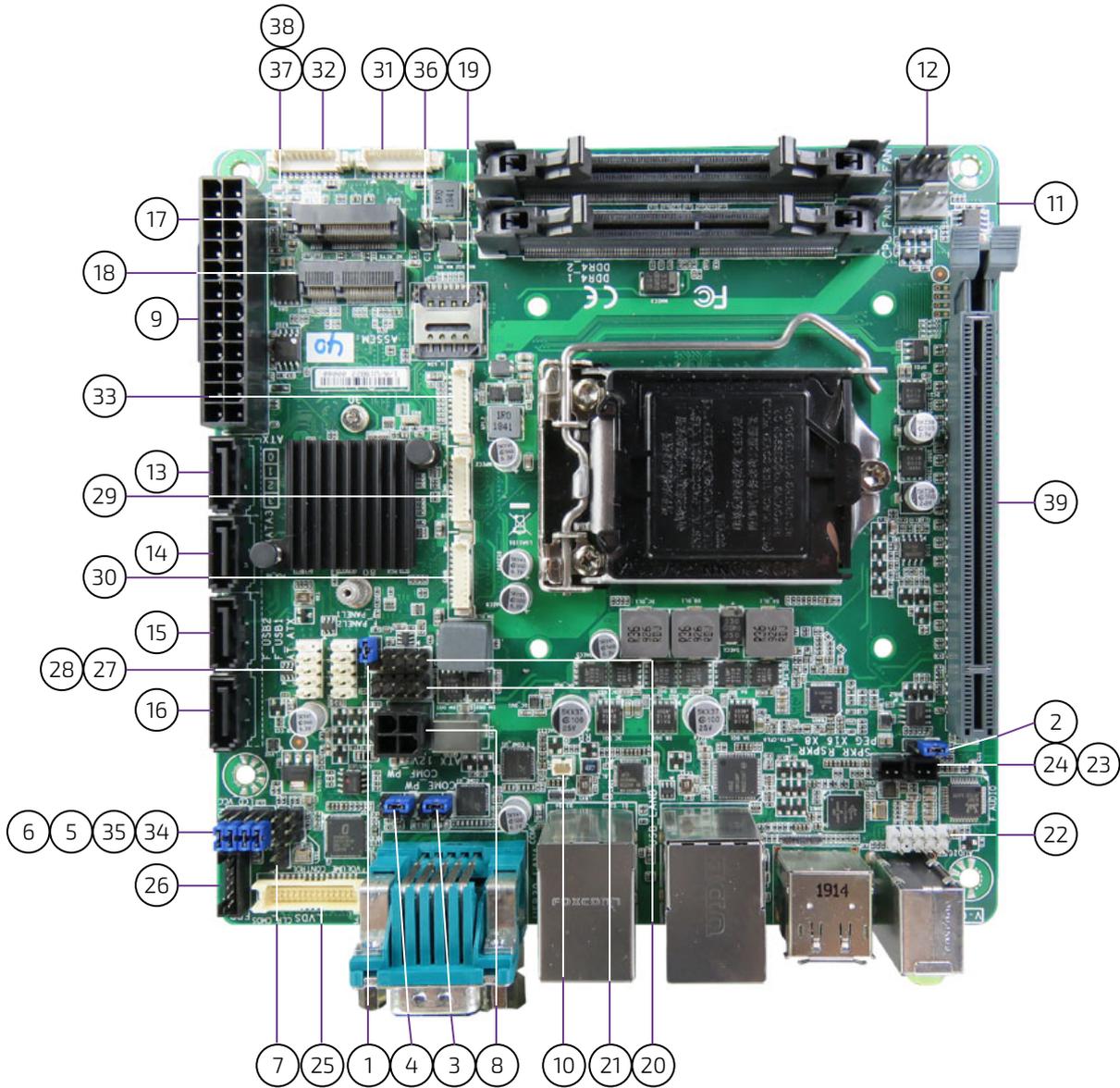
Thank you for purchasing the Kontron MITX-CFLO Series embedded board. This document provides information to allow you to quickly install this product.

Packing Checklist

1. Take the MITX-CFLO Series out of the packing box and check if the unit is properly secure in the plastic bag.
2. Check the contents of the carton box: (*: optional)

- ▶ MITX-CFLO Series main board
- ▶ Quick Installation Guide
- ▶ I/O Shield
- ▶ SATA Cable *
- ▶ Audio Cable *
- ▶ Speaker Cable *
- ▶ USB 2.0 Cable *
- ▶ COM Port Cable *
- ▶ DIO Cable *
- ▶ Active Cooler * (type depending on ordered configuration)

Top Side Jumper and Internal Connector Locations



► Jumper List

Item	Designation	Description
1	AT_ATX	AT / ATX Power Mode Selection
2	PEG_X16_X8	PCIe Configuration Setting for PCIEX16
3	COME_PW	Pin-9 Power Selection for COM 5
4	COMF_PW	Pin-9 Power Selection for COM 6
5	LCD_VCC	Panel & Backlight Power Selection for LVDS
6	BL_EN	Backlight Power Enable Selection for LVDS
7	CLR_CMOS	Clear CMOS Selection

► Internal Connector List

Item	Designation	Description
8	ATX_12V	2x2-pin ATX Power Supply Wafer
9	ATX	2x12-pin ATX Power Supply Wafer
10	BAT	RTC Power Input Wafer
11	CPU_FAN	CPU FAN Wafer
12	SYS_FAN	System FAN Wafer
13	SATA3_0	Serial ATA 3.0 Port-0 Connector
14	SATA3_1	Serial ATA 3.0 Port-1 Connector
15	SATA3_2	Serial ATA 3.0 Port-2 Connector
16	SATA3_3	Serial ATA 3.0 Port-3 Connector
17	M2_KEYB	M.2 Key B Socket
18	-	M.2 Key E Socket
19	U_SIM	Micro SIM Interface Slot for M.2 Key B
20	F_PANEL1	Front Panel 1 Pin Header
21	F_PANEL2	Front Panel 2 Pin Header
22	F_AUDIO	Front Panel Audio Pin Header
23	SPKR_R	Right Channel 3W Audio AMP Output Wafer
24	SPKR_L	Left Channel 3W Audio AMP Output Wafer
25	LVDS	Primary 24-bit, 2-channel LVDS Panel Connector
26	FPD	Panel Backlight Wafer for LVDS
27	F_USB1	USB 2.0 Port 4 & Port 5 Pin Header
28	F_USB2	USB 2.0 Port 6 & Port 7 Pin Header
29	COMA	RS232 Port 1 Wafer
30	COMB	RS232 Port 2 Wafer
31	COMC	RS232 Port 3 Wafer
32	COMD	RS232 Port 4 Wafer
33	GPIO	Digital Input / Output Wafer
34	VOLUME_CONTROL	Volume Control Pin Header
35	I2C	I2C (Inter-Integrated Circuit) Pin Header
36	CI	Chassis Intrusion Pin Header
37	STB	Standby Status LED
38	BOOT	Boot Status LED
39	PCIEX16	PCI Express x16 Slot

Jumper Setting

▶ AT / ATX Power Mode Selection (AT_ATX)



Jumper Position		Description
Pin 1-2	Pin 2-3	
X	-	AT Mode
-	X	ATX Mode (Default)

"X" = Jumper set (short) and "-" = jumper not set (open)

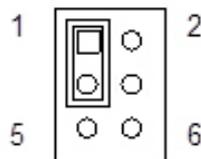
▶ PCIe Configuration Setting for PCIEX16 (PEG_X16_X8)



Jumper Position		Description
Pin 1-2	Pin 2-3	
X	-	PCIe x16 (Default)
-	X	PCIe x8 + PCIe x8

"X" = Jumper set (short) and "-" = jumper not set (open)

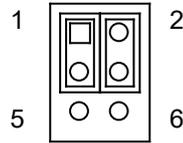
▶ Pin-9 Power Selection for COM 5 & COM 6 (COME_PW & COMF_PW)



Jumper Position			Description
Pin 1-3	Pin 3-5	Pin 3-4	
X	-	-	Pin-9 = 5V
-	X	-	Pin-9 = 12V
-	-	-	Pin-9 = RI

"X" = Jumper set (short) and "-" = jumper not set (open)

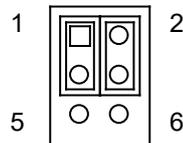
▶ Panel & Backlight Power Selection for LVDS (LCD_VCC)



Jumper 1 Position		Description
Pin 1-3	Pin 3-5	
X	-	Backlight Power = +12 V (Default)
-	X	Backlight Power = +5 V
Jumper 2 Position		Description
Pin 2-4	Pin 4-6	
X	-	Panel Power = +3.3 V (Default)
-	X	Panel Power = +5 V

"X" = Jumper set (short) and "-" = jumper not set (open)

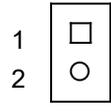
▶ Backlight Power Enable Selection for LVDS (BL_EN)



Jumper 1 Position		Description
Pin 1-3	Pin 3-5	
X	-	Backlight Enable Voltage = +3.3 V (Default)
-	X	Backlight Enable Level = +5 V
Jumper 2 Position		Description
Pin 2-4	Pin 4-6	
X	-	Backlight Enable Active High (Default)
-	X	Backlight Enable Active Low

"X" = Jumper set (short) and "-" = jumper not set (open)

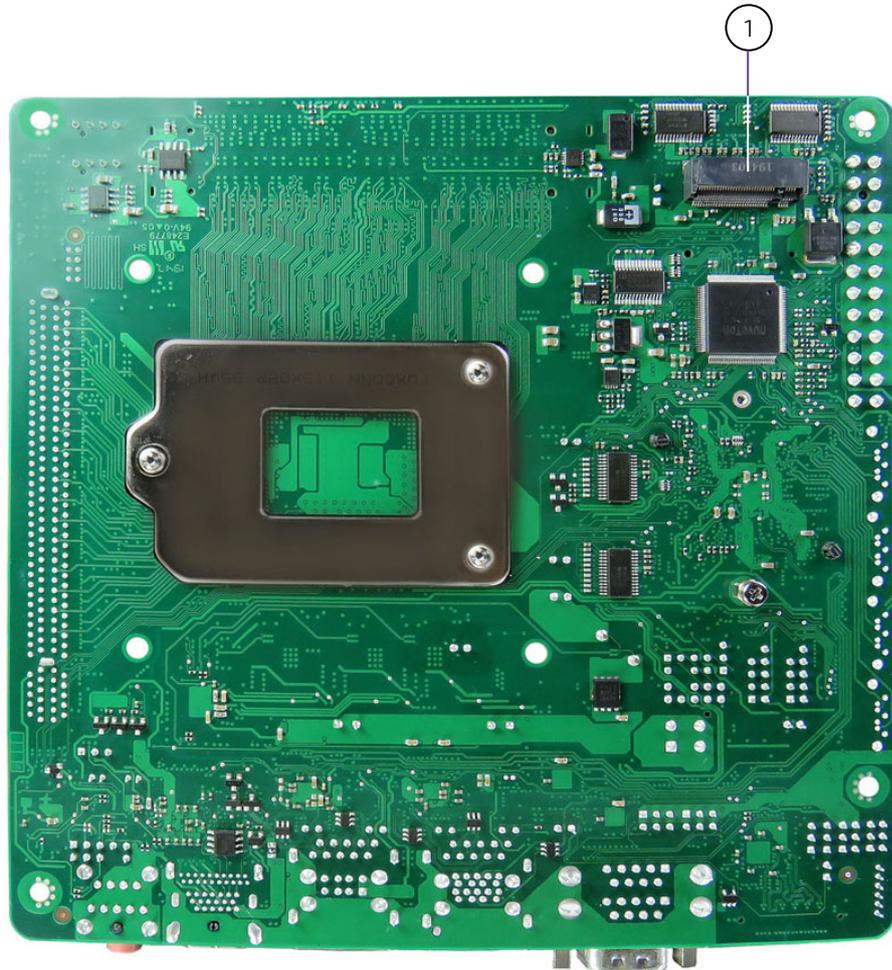
▶ Clear CMOS Selection (CLR_CMOS)



Jumper Position	Description
Pin 1-2	
-	Normal operation (default position)
X	Enable Clear CMOS RTC content (board does not boot with the jumper in this position)

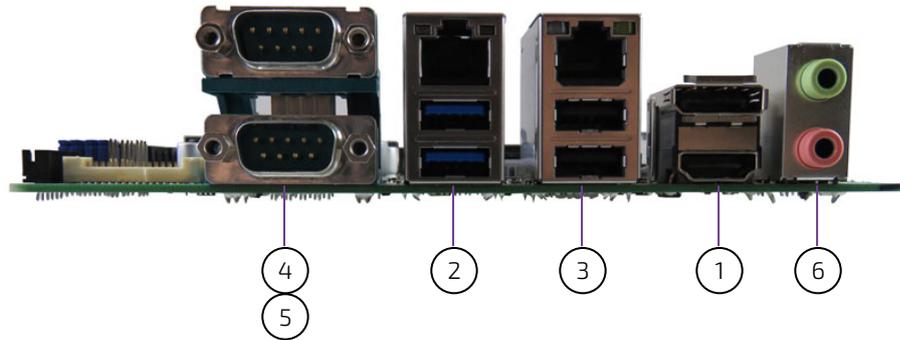
"X" = Jumper set (short) and "-" = jumper not set (open)

Bottom Side Internal Connector Locations



Item	Designation	Description
1	-	M.2 Key M Socket

Rear I/O Panel Connector Locations



Item	Designation	Description
1	DP_HDMI	DP++ & HDMI 2.0 Connector
2	USB30_LANA	GbE LAN A & USB 3.0 Port-0, 1 Connector
3	USB_LANB	GbE LAN B & USB 2.0 Port-2, 3 Connector
4	COME	RS232/422/485 COM 5 Connector
5	COMF	RS232/422/485 COM 6 Connector
6	AUDIO	Line-Out & Mic-In Audio Jacks