



*Errata***1**

Manual: 24855

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1. Reference: Page 4 - 14, Chapter 4.3.4.7, Table 4-12

The referenced information (table) is superseded by the following table.

Table 4-12: Control Register

REGISTER NAME		CONTROL						ACCESS			
ADDRESS		0xFFE0 001A						R	W		
BIT POSITION		MSB	7	6	5	4	3	2	1	0	LSB
CONTENT		RS_CTL	Res.	VRST_DIS	S_RST	Res.	I_DEL	GPLED2	GPLED1		
DEFAULT		n/a	n/a	0	n/a	0	0	0	0		
BIT	NAME	VAL	DESCRIPTION								
0	Indicator LED1/ Green	0	GPLED1 LED1G (green) off								
		1	GPLED1 LED1G (green) on								
1	Indicator LED2/Red	0	GPLED2 (optional, red) off								
		1	GPLED2 (optional, red) on								
2	I_DEL	0	Init-Delay off (default)								
		1	Init-Delay on								
3	Res.	0	Reserved								
		1									
4	S_RST	0	No operation								
		1	Causes a software reset (S_RST) to be initiated								
5	VRST_DIS	0	VME-Reset-Disable off (default)								
		1	VME-Reset-Disable on								
6	Res.	0	Reserved								
		1									
7	RS_CTL	0	SER connector configured to act as an RS232 interface (default)								
		1	SER connector configured to act as an RS485 interface								

2. Reference: Page A - 12, Chapter A.8, Table A-4

The referenced information (table) is superseded by the following table:

Table A-4: IO1 Jumper Settings for Different Module Positions

	IDSEL			Clock			GNT#			REQ#		
	J12	J11	J10	J1	J2	J3	J4	J5	J6	J7	J8	J9
Position 1	Set	Open	Open	Set	Open	Open	Set	Open	Open	Set	Open	Open
Position 2	Open	Set	Open	Open	Set	Open	Open	Set	Open	Open	Set	Open

Position 3 has been deleted.

3. Reference: Page A - 12, Chapter A.8, Note

The referenced information (note) is superseded by the following note:



Note ...

Position 1 refers to the settings applicable when 1 module (IO1 or other) is attached to the VMP2.

Position 2 refers to the settings applicable when 2 modules (IO1 or other) are attached to the VMP2.