



KBox N-120-ADN

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 KBOX N-120-ADN - USER GUIDE

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Revision History

Revision	Brief Description of Changes	Date of Issue	Author/Editor
1.0	Initial Issue	2025-Aug-05	YS

Terms and Conditions

Kontron warrants products in accordance with defined regional warranty periods. For more information about warranty compliance and conformity, and the warranty period in your region, visit <https://www.kontron.com/terms-and-conditions>.

Kontron sells products worldwide and declares regional General Terms & Conditions of Sale, and Purchase Order Terms & Conditions. Visit <https://www.kontron.com/terms-and-conditions>.

For contact information, refer to the corporate offices contact information on the last page of this user guide or visit our website [CONTACT US](#).

Customer Support

Find Kontron contacts by visiting: <https://www.kontron.com/support>.

Customer Service

As a trusted technology innovator and global solutions provider, Kontron extends its embedded market strengths into a services portfolio allowing companies to break the barriers of traditional product lifecycles. Proven product expertise coupled with collaborative and highly-experienced support enables Kontron to provide exceptional peace of mind to build and maintain successful products.

For more details on Kontron's service offerings such as: enhanced repair services, extended warranty, Kontron training academy, and more visit <https://www.kontron.com/support-and-services/services>.

Customer Comments

If you have any difficulties using this user guide, discover an error, or just want to provide some feedback, contact [Kontron support](#). Detail any errors you find. We will correct the errors or problems as soon as possible and post the revised user guide on our website.

Symbols

The following symbols may be used in this user guide

⚠ DANGER

DANGER indicates a hazardous situation which, if not avoided, will result in death or serious injury.

⚠ WARNING

WARNING indicates a hazardous situation which, if not avoided, could result in death or serious injury.

NOTICE

NOTICE indicates a property damage message.

⚠ CAUTION

CAUTION indicates a hazardous situation which, if not avoided, may result in minor or moderate injury.



Electric Shock!

This symbol and title warn of hazards due to electrical shocks (> 60 V) when touching products or parts of products. Failure to observe the precautions indicated and/or prescribed by the law may endanger your life/health and/or result in damage to your material.



ESD Sensitive Device!

This symbol and title inform that the electronic boards and their components are sensitive to static electricity. Care must therefore be taken during all handling operations and inspections of this product in order to ensure product integrity at all times.



HOT Surface!

Do NOT touch! Allow to cool before servicing.



Laser!

This symbol inform of the risk of exposure to laser beam and light emitting devices (LEDs) from an electrical device. Eye protection per manufacturer notice shall review before servicing.



This symbol indicates general information about the product and the user guide.

This symbol also indicates detail information about the specific product configuration.



This symbol precedes helpful hints and tips for daily use.

For Your Safety

Your new Kontron product was developed and tested carefully to provide all features necessary to ensure its compliance with electrical safety requirements. It was also designed for a long fault-free life. However, the life expectancy of your product can be drastically reduced by improper treatment during unpacking and installation. Therefore, in the interest of your own safety and of the correct operation of your new Kontron product, you are requested to conform with the following guidelines.

High Voltage Safety Instructions

As a precaution and in case of danger, the power connector must be easily accessible. The power connector is the product's main disconnect device.

CAUTION

Warning

All operations on this product must be carried out by sufficiently skilled personnel only.

CAUTION



Electric Shock!

Before installing a non hot-swappable Kontron product into a system always ensure that your mains power is switched off. This also applies to the installation of piggybacks. Serious electrical shock hazards can exist during all installation, repair, and maintenance operations on this product. Therefore, always unplug the power cable and any other cables which provide external voltages before performing any work on this product.

Earth ground connection to vehicle's chassis or a central grounding point shall remain connected. The earth ground cable shall be the last cable to be disconnected or the first cable to be connected when performing installation or removal procedures on this product.

Special Handling and Unpacking Instruction

NOTICE



ESD Sensitive Device!

Electronic boards and their components are sensitive to static electricity. Therefore, care must be taken during all handling operations and inspections of this product, in order to ensure product integrity at all times.

Do not handle this product out of its protective enclosure while it is not used for operational purposes unless it is otherwise protected.

Whenever possible, unpack or pack this product only at EOS/ESD safe work stations. Where a safe work station is not guaranteed, it is important for the user to be electrically discharged before touching the product with his/her hands or tools. This is most easily done by touching a metal part of your system housing.

It is particularly important to observe standard anti-static precautions when changing piggybacks, ROM devices, jumper settings etc. If the product contains batteries for RTC or memory backup, ensure that the product is not placed on conductive surfaces, including anti-static plastics or sponges. They can cause short circuits and damage the batteries or conductive circuits on the product.

Lithium Battery Precautions

If your product is equipped with a lithium battery, take the following precautions when replacing the battery.

CAUTION

Danger of explosion if the battery is replaced incorrectly.

- ▶ Replace only with same or equivalent battery type recommended by the manufacturer.
- ▶ Dispose of used batteries according to the manufacturer's instructions.

General Instructions on Usage

In order to maintain Kontron's product warranty, this product must not be altered or modified in any way. Changes or modifications to the product, that are not explicitly approved by Kontron and described in this user guide or received from Kontron Support as a special handling instruction, will void your warranty.

This product should only be installed in or connected to systems that fulfill all necessary technical and specific environmental requirements. This also applies to the operational temperature range of the specific board version that must not be exceeded. If batteries are present, their temperature restrictions must be taken into account.

In performing all necessary installation and application operations, only follow the instructions supplied by the present user guide.

Keep all the original packaging material for future storage or warranty shipments. If it is necessary to store or ship the product then re-pack it in the same manner as it was delivered.

Special care is necessary when handling or unpacking the product. See Special Handling and Unpacking Instruction.

Quality and Environmental Management

Kontron aims to deliver reliable high-end products designed and built for quality, and aims to complying with environmental laws, regulations, and other environmentally oriented requirements. For more information regarding Kontron's quality and environmental responsibilities, visit <https://www.kontron.com/about-kontron/corporate-responsibility/quality-management>.

Disposal and Recycling

Kontron's products are manufactured to satisfy environmental protection requirements where possible. Many of the components used are capable of being recycled. Final disposal of this product after its service life must be accomplished in accordance with applicable country, state, or local laws or regulations.

WEEE Compliance

The Waste Electrical and Electronic Equipment (WEEE) Directive aims to:

- ▶ Reduce waste arising from electrical and electronic equipment (EEE)
- ▶ Make producers of EEE responsible for the environmental impact of their products, especially when the product become waste
- ▶ Encourage separate collection and subsequent treatment, reuse, recovery, recycling and sound environmental disposal of EEE
- ▶ Improve the environmental performance of all those involved during the lifecycle of EEE



Environmental protection is a high priority with Kontron.

Kontron follows the WEEE directive

You are encouraged to return our products for proper disposal.

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1/ General Safety Instructions for IT Equipment

⚠ WARNING

Please read this chapter carefully and take careful note of the instructions, which have been compiled for your safety and to ensure to apply in accordance with intended regulations. If the following general safety instructions are not observed, it could lead to injuries to the operator and/or damage of the product; in cases of nonobservance of the instructions Kontron is exempt from accident liability, this also applies during the warranty period.

The product has been built and tested according to the basic safety requirements for low voltage (LVD) applications and has left the manufacturer in safety-related, flawless condition. To maintain this condition and also to ensure safe operation, the operator must not only observe the correct operating conditions for the product but also the following general safety instructions:

- ▶ The product must be used as specified in the product documentation, in which the instructions for safety for the product and for the operator are described. These contain guidelines for setting up, installation and assembly, maintenance, transport or storage.
- ▶ The on-site electrical installation must meet the requirements of the country's specific local regulations.
- ▶ If a power cable comes with the product, only this cable should be used. Do not use an extension cable to connect the product.
- ▶ To guarantee that sufficient air circulation is available to cool the product, please ensure that the ventilation openings are not covered or blocked. If an air filter is provided, this should be cleaned regularly. Do not place the system close to heat sources or damp places. Make sure the system is well ventilated.
- ▶ Only devices or parts which fulfill the requirements of SELV circuits (Safety Extra Low Voltage) as stipulated by IEC 60950-1 may be connected to the available interfaces.
- ▶ Before opening the device, make sure that the device is disconnected from the mains.
- ▶ Switching off the device by its power button does not disconnect it from the mains. Complete disconnection is only possible if the power cable is removed from the wall plug or from the device. Ensure that there is free and easy access to enable disconnection.
- ▶ The device may only be opened for the insertion or removal of add-on cards (depending on the configuration of the system). This may only be carried out by qualified operators.
- ▶ If extensions are being carried out, the following must be observed:
 - ▶ All effective legal regulations and all technical data are adhered to.
 - ▶ The power consumption of any add-on card does not exceed the specified limitations.
 - ▶ The current consumption of the system does not exceed the value stated on the product label.
- ▶ Only original accessories that have been approved by Kontron can be used.
- ▶ Please note: safe operation is no longer possible when any of the following applies:
 - ▶ The device has visible damages.
 - ▶ The device is no longer functioning.

In this case the device must be switched off and it must be ensured that the device can no longer be operated.

Additional safety instructions for DC power supply circuits

- ▶ To guarantee safe operation of devices with DC power supply voltages larger than 60 volts DC or a power consumption larger than 240 VA, please observe that:
 - ▶ the device is set up, installed and operated in a room or enclosure marked with "RESTRICTED ACCESS", if there are no safety messages on product as safety signs and labels on the device itself.
 - ▶ no cables or parts without insulation in electrical circuits with dangerous voltage or power should be touched directly or indirectly
 - ▶ a reliable protective earthing connection is provided
 - ▶ a suitable, easily accessible disconnecting device is used in the application (e.g. overcurrent protective device), if the device itself is not disconnectable
 - ▶ a disconnect device, if provided in or as part of the equipment, shall disconnect both poles simultaneously
 - ▶ interconnecting power circuits of different devices cause no electrical hazards
- ▶ A sufficient dimensioning of the power cable wires must be selected – according to the maximum electrical specifications on the product label – as stipulated by EN60950-1 or VDE0100 or EN60204 or UL508 regulations.
- ▶ The devices do not generally fulfill the requirements for "centralized DC power systems" (UL 60950-1, Annex NAB; D2) and therefore may not be connected to such devices!

1.1. Electrostatic Discharge (ESD)



A sudden discharge of electrostatic electricity can destroy static-sensitive devices or micro-circuitry.

Therefore proper packaging and grounding techniques are necessary precautions to prevent damage. Always take the following precautions:

1. Transport boards in ESD-safe containers such as boxes or bags.
2. Keep electrostatic sensitive parts in their containers until they arrive at the ESD-safe workplace.
3. Always be properly grounded when touching a sensitive board, component, or assembly.
4. Store electrostatic-sensitive boards in protective packaging or on antistatic mats.

1.1.1. Grounding Methods

By adhering to the guidelines below, electrostatic damage to the device can be avoided:

1. Cover workstations with approved antistatic material. Always wear a wrist strap connected to workplace. Always use properly grounded tools and equipment.
2. Use antistatic mats, heel straps, or air ionizers for more protection.
3. Always handle electrostatically sensitive components by their edge or by their casing.
4. Avoid contact with pins, leads, or circuitry.
5. Turn off power and input signals before inserting and removing connectors or connecting test equipment.
6. Keep work area free of non-conductive materials such as ordinary plastic assembly aids and Styrofoam.
7. Use only field service tools which are conductive, such as cutters, screwdrivers, and vacuum cleaners.
8. Always place drives and boards PCB-assembly-side down on the foam.

1.2. Lithium Battery Replacement

If replacing the lithium battery, follow the replacement precautions stated below.

⚠ WARNING

Do not ingest battery, Chemical Burn Hazard

This product contains a coin / button cell battery. If the coin / button cell battery is swallowed, it can cause severe internal burns in just 2 hours and can lead to death.

Keep new and used batteries away from children.

If the battery compartment does not close securely, stop using the product and keep it away from children.

If you think batteries might have been swallowed or placed inside any part of the body, seek immediate medical attention.

Replacement of a battery with an incorrect type, that can result in an explosion. Replace only with the same or equivalent type recommended by the manufacturer. The lithium battery type must be UL recognized.

Disposal of a battery into fire or a hot oven, or mechanically crushing or cutting of a battery, that can result in an explosion.

Leaving a battery in an extremely high temperature surrounding environment that can result in an explosion or the leakage of flammable liquid or gas

A battery subjected to extremely low air pressure that may result in an explosion or the leakage of flammable liquid or gas



Do not dispose of lithium batteries in general trash collection. Dispose of the battery according to the local regulations dealing with the disposal of these special materials, (e.g. to the collecting points for dispose of batteries).

2/ Electromagnetic Compatibility

For detailed information refer to section 10.3 "CE Directives and Standards".

2.1. Electromagnetic Compatibility (EU)

This product has been designed for low level of radiated emission for residential, commercial and light industrial environments and high immunity level for industrial environmental. This product complies with the European Council Directive on the approximation of the laws of the member states relating to electromagnetic compatibility (EMC Directive 2014/30/EU) and Radio Equipment Directive (RED Directive 2014/53/EU).

▲ WARNING

This is a class B product. In domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

2.2. FCC Statement (USA)

The following statement applies to the products covered in this manual, unless otherwise specified herein. The statement for other products will appear in the accompanying documentation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation.

If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- ▶ Reorient or relocate the receiving antenna.
- ▶ Increase the separation between the equipment and receiver.
- ▶ Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- ▶ Consult the dealer or an experienced radio/TV technician for help.

Kontron is not responsible for any radio television interference caused by unauthorized modifications of this equipment or the substitution or attachment of connecting cables and equipment other than those specified by Kontron. The correction of interference caused by such unauthorized modification, substitution or attachment will be the responsibility of the user.

The use of shielded I/O cables is required when connecting this equipment to any and all optional peripheral or host devices. Failure to do so may violate FCC and ICES rules.

2.3. EMC Compliance (Canada)

The method of compliance is self-declaration to Canadian standard ICES-003:

(English): This Class B digital apparatus complies with the Canadian ICES-003.

(French): Cet appareil numérique de la class B est conforme à la norme NMB-003 du Canada.

3/ Shipment and Unpacking

Please check that your package is complete, and contains the items below (according to the ordered unit configuration). If you discover damaged or missing items, please contact your dealer.

3.1. Unpacking

Proceed as follows to unpack the unit:

1. Remove packaging.
2. Do not discard the original packaging. Keep it for future relocation.
3. Check the delivery for completeness by comparing it with your order.
4. Please keep the associated paperwork. It contains important information for handling the unit.
5. Check the contents for visible shipping damage.
6. If you notice any shipping damage or inconsistencies between the contents and your order, please contact Kontron for help and information.

3.2. Scope of Delivery

3.2.1. Standard

- ▶ 1x KBox N-120-ADN (corresponding to the ordered system configuration)
- ▶ 1x Quick installation guide
- ▶ 1x Driver CD

3.2.2. Optional Parts

- ▶ 1x Power adapter
- ▶ 1x Power cord (plug type depending on country)
- ▶ Memory module (It may be pre-installed in the system depending on ordered configuration.)
- ▶ 2.5" HDD / SSD (It may be pre-installed in the system depending on ordered configuration.)
- ▶ M.2 Key B expansion card or SSD (It may be pre-installed in the system depending on ordered configuration.)
- ▶ M.2 Key E expansion card (It may be pre-installed in the system depending on ordered configuration.)
- ▶ M.2 Key M SSD (It may be pre-installed in the system depending on ordered configuration.)
- ▶ Antenna(s)
- ▶ VESA mount kit
- ▶ Rackmount kit

4/ System Overview

The KBox N-120-ADN is a fanless system enclosed within a compact aluminum chassis with cooling fins, offering the superior qualities for network security controls.

It can be optionally factory-equipped with an M.2 Key B WWAN and /or M.2 Key E WLAN / Bluetooth card for two antennas each. Users may choose the implementation of a 2.5" SATA HDD / SSD or of a M.2 Key B / Key M SSD card as storage media.

The following interfaces are available with the KBox N-120-ADN:

Standard Front Panel:

- ▶ 1x Power LED
- ▶ 1x M.2 Key M SSD Storage LED
- ▶ 2x Act / Link LED
- ▶ 2x Speed LED

Standard Rear Panel:

- ▶ 2x 2.5 GbE LAN
- ▶ 2x DisplayPort
- ▶ 1x DisplayPort over USB 3.2 Gen 2 Type C
- ▶ 2x USB 3.2 Gen 2 Type A
- ▶ 1x USB 2.0 Type A
- ▶ 1x Lockable DC-In Jack
- ▶ 1x Power Button
- ▶ 1x Power LED
- ▶ 1x Standby LED
- ▶ 2x Wi-Fi Antenna Port
- ▶ 2x 3G / 4G / 5G Antenna Port
- ▶ 1x DB9 Cutout reserved for RS232/422/485

Standard Baseboard and System Expansion Capabilities:

- ▶ 1x DDR5 SO-DIMM memory socket (DIMM1)
- ▶ 1x 7-pin SATA data connector (CN10) + 1x 4-pin SATA power wafer (CN8) for 2.5" SATA HDD / SSD
- ▶ 1x M.2 Key B socket (type 22x42 / 30x42 / 30x52 / 22x80, M2B1) for SSD / WWAN expansion
- ▶ 1x M.2 Key E socket (type 22x30, M2E1) for WLAN / Bluetooth expansion
- ▶ 1x M.2 Key M socket (type 22x80, M2M1) for SSD
- ▶ 1x SIM Card Wafer (CN3) with connection to M.2 Key B socket (M2B1)

The device is designed to be operated in:

- ▶ Vertical / horizontal position: mounted on the back of the monitor or inside a control cabinet / custom enclosure / machine with (with a VESA mounting kit) or
- ▶ Horizontal position: mounted in a rack or cabinet (with a rackmount kit) or
- ▶ Horizontal position: placed as a desktop unit.

NOTICE

When powering on the KBox N-120-ADN, make sure that the cooling fins of the chassis are not obstructed (covered) by any objects.

To provide sufficient heat dissipation by the cooling of the device, do not cover the cooling fins of the KBox N-120-ADN. Do not place any objects on the device. When installing the system, please keep clearance for air circulation.

4.1. System Expansion Capabilities

4.1.1. System Expansion via SATA Interface

The baseboard comes with an onboard SATA 3.0 interface connector and a corresponding power connector. Users can expand the KBox N-120-ADN with a 2.5" SATA HDD / SSD drive.

4.1.2. System Expansion via M.2 Card Interface

The baseboard comes with three onboard M.2 interface connectors with Key B Type 2242 / 3042 / 3052 / 2280, Key E Type 2230 and Key M Type 2280 support respectively. The Key B connector is intended to be used to install a M.2 WWAN expansion card or a M.2 SSD card drive, the Key E connector to install a M.2 WLAN / Bluetooth / combo expansion card, and the Key M connector to install a M.2 SSD card drive.

4.1.3. System Expansion via SIM Card Wafer

The baseboard comes with an onboard wafer to expand a SIM card holder module for accommodating a SIM card in case of no SIM card holder mounted on the M.2 WWAN expansion card.

In order to use the SIM card reader functionality, a corresponding M.2 WWAN expansion card must be installed to the M.2 Key B socket of your KBox N-120-ADN.

4.2. Front I/O Panel

Figure 1: Front I/O Panel

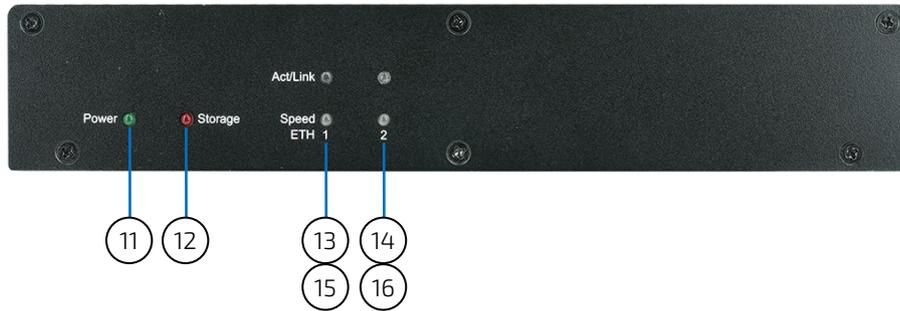
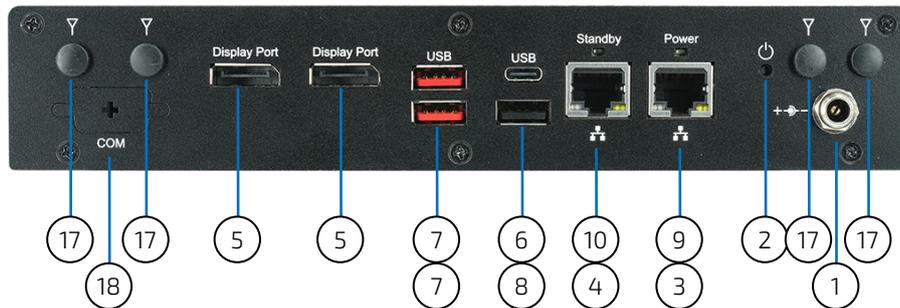


Figure 2: Rear I/O Panel



- 1 DC-In (see Chapter 4.2.1)
- 2 Power Button (See Chapter 4.2.2)
- 3 2.5 GbE LAN 1 (see Chapter 4.2.3)
- 4 2.5 GbE LAN 2 (see Chapter 4.2.3)
- 5 DisplayPort (see Chapter 4.2.4)
- 6 DisplayPort over USB 3.2 Gen 2 Type C (see Chapter 4.2.5)
- 7 USB 3.2 Gen 2 Type A (see Chapter 4.2.6)
- 8 USB 2.0 Type A (see Chapter 4.2.7)
- 9 Power LED (see Chapter 4.2.8)
- 10 Standby LED (see Chapter 4.2.8)
- 11 Power LED (see Chapter 4.2.9)
- 12 Storage LED (see Chapter 4.2.10)
- 13 LAN 1 Act / Link LED (see Chapter 4.2.11)
- 14 LAN 2 Act / Link LED (see Chapter 4.2.11)
- 15 LAN 1 Speed LED (see Chapter 4.2.12)
- 16 LAN 2 Speed LED (see Chapter 4.2.12)
- 17 Antenna Port (see Chapter 4.2.13)

18 Cutout reserved for RS232/422/485 or DIO (see Chapter 4.2.14)

4.2.1. DC-In (Lockable DC Jack)

Power supplied through this jack supplies power to the system. To prevent damage to the system, use the supplied power adapter if you order it from Kontron or a compatible third-party adapter from a reputable source for converting AC power to DC power for use with this jack.

▲WARNING

Adapter's power cord shall be connected to a socket-outlet with earthing connection.

4.2.2. Power Button

Use the tip end of a pen to press this button to turn the system on or to put the system in sleep or soft-off mode. When pressing this button for more than four seconds, the system turns from ON to OFF.

▲WARNING

Even when the system is turned off via the power button there is still a standby voltage on the baseboard. The unit is only completely disconnected from the DC mains, when the power is removed.

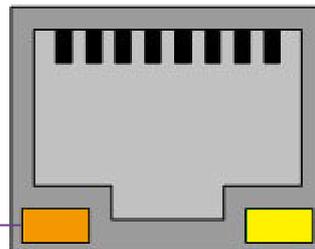
4.2.3. 2.5 GbE LAN

The KBox N-120-ADN provides two 2.5 GbE LAN ports on the rear I/O panel. These connectors are 2.5 Gigabit Ethernet 10/100/1000/2500 Mbit/s, IEEE 1588 capable interfaces. The connectors are standard 8-pin RJ45 type connectors with status LEDs:

Figure 3: 2.5 Gigabit Ethernet LED Status

LED status:

- Orange - 1000 Mbit/s link established
- Green - 2.5 Gbit/s link established
- Off - 10/100 Mbit/s link established



LED status:

- Off - Link is down
- Flashing Yellow - Link is up and active
- Steady Yellow - Link is up, no activity

4.2.4. DisplayPort

An external (digital) display can be connected to this DisplayPort connector.

4.2.5. DisplayPort over USB 3.2 Gen 2 Type C

The KBox N-120-ADN provides a DisplayPort over USB 3.2 Gen 2 Type C connector on the rear I/O panel. The connector supports USB DisplayPort Alternate mode for transmitting video signals, USB 3.2 Gen 2 for high-speed data transfer and power delivery of up to 15 W (5 V at 3 A) for charging a connected device.

4.2.6. USB 3.2 Gen 2

The KBox N-120-ADN provides two USB 3.2 Gen 2 connectors on the rear I/O panel. These connectors are backward compatible with USB 3.2 Gen 1 / USB 2.0 and allow connection of USB-compatible devices to the system.

4.2.7. USB 2.0

The KBox N-120-ADN provides an USB 2.0 connector on the rear I/O panel. The connector allows connection of an USB-compatible device to the system.

4.2.8. Power LED & Standby LED

The KBox N-120-ADN has a power LED and standby LED on the rear I/O panel. System modes can be identified through these two LED indicators. Refer to the below table for more details.

Prerequisite: The system must be attached by means of the power cord to an appropriate main power supply (DC).

Table 1: Power LED & Standby LED Status

LED Status		Description
Power LED	Standby LED	
Green LED On	Yellow LED On	S0 (Full On)
Green LED Blink	Yellow LED On	S3 (Suspend-To-RAM)
LED Off	Yellow LED On	S4 (Suspend-To-Disk) or S5 (Soft Off)
LED Off	LED Off	EUP Mode or G3 (Mechanical Off)

4.2.9. Power LED

The KBox N-120-ADN has another power LED on the front I/O panel. The power LED lights up green if the system powered on.

Prerequisite: The system must be attached by means of the power cord to an appropriate main power supply (DC).

4.2.10. Storage LED

The KBox N-120-ADN has a storage LED on the front I/O panel. The storage LED blinks red when the data is being written into or read from the M.2 Key M SSD drive.

4.2.11. LAN Act / Link LED

The KBox N-120-ADN has two LAN Act / Link LEDs on the front I/O panel for LAN 1 and LAN 2 respectively. The LAN Act / Link LED indicates whether the interface is connected to a network and whether data is being transmitted or received over the network.

Table 2: LAN Act / Link LED Status

LED Status	Description
Off	Link is down

Flashing Yellow	Link is up and active
Steady Yellow	Link is up, no activity

4.2.12. LAN Speed LED

The KBox N-120-ADN has two LAN Speed LEDs on the front I/O panel for LAN 1 and LAN 2 respectively. The color that the speed LED lights up indicates the link speed of the Ethernet connection.

Table 3: LAN Speed LED Status

LED Status	Description
Orange	1000 Mbit/s link established
Green	2.5 Gbit/s link established
Off	10/100 Mbit/s link established

4.2.13. Antenna Port

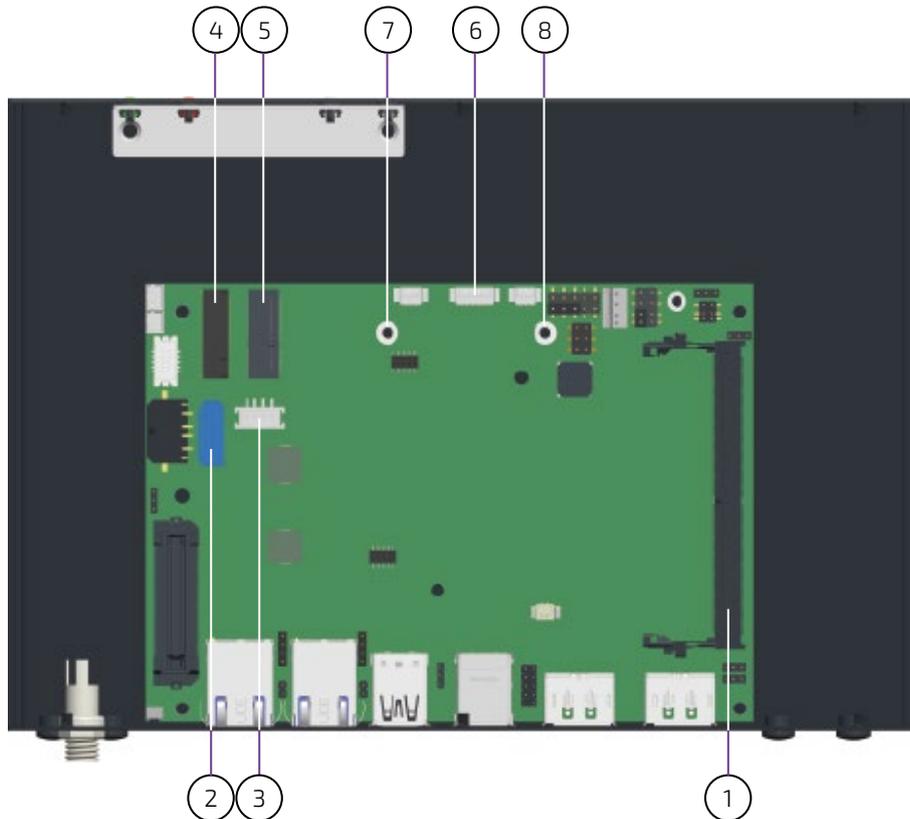
The KBox N-120-ADN reserves two pairs of covered cutouts for the Reverse (RP) SMA connectors of the WLAN antennas (M.2 Key E WLAN card with one or two antennas is an option) and WWAN antenna (M.2 Key B WWAN card with one or two antennas is an option) respectively.

4.2.14. Cutout Reserved for RS232/422/485 or DIO

The KBox N-120-ADN reserves a DB9 cutout for additional support of RS232/422/485 serial communication or digital I/O connections.

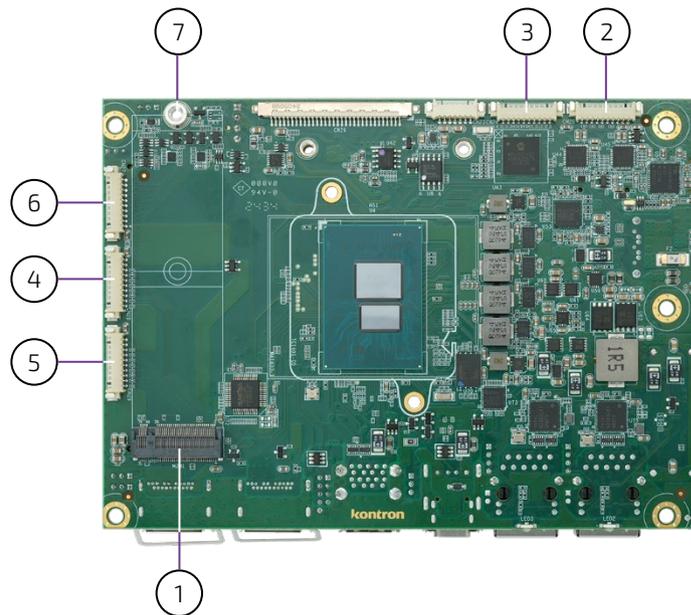
4.3. Internal View

Figure 4: Internal View (without bottom cover)



- 1 DDR5 SO-DIMM Memory Socket (DIMM1, see Chapter 4.3.1)
- 2 SATA Data Connector (CN10, see Chapter 4.3.2)
- 3 SATA Power Output Connector (CN8, see Chapter 4.3.2)
- 4 M.2 Key B Socket (M2B1, see Chapter 4.3.3)
- 5 M.2 Key E Socket (M2E1, see Chapter 4.3.4)
- 6 SIM Card Wafer (CN3, see Chapter 4.3.6)
- 7 M.2 Fixing Bolt for Key B Type 22x42 / 30x42 (/30x52) and / or Key E Type 22x30
- 8 M.2 Fixing Bolt for Key B Type 22x80 (/30x52)

Figure 5: Internal View (top side of the baseboard)



- 1 M.2 Key M Socket (M2M1, see Chapter 4.3.5)
- 2 RS232/422/485 COM1 Wafer (CN25, see Chapter 4.3.7)
- 3 RS232/422/485 COM2 Wafer (CN24, see Chapter 4.3.7)
- 4 RS232/422/485 COM3 Wafer (CN28, see Chapter 4.3.7)
- 5 RS232/422/485 COM4 Wafer (CN29, see Chapter 4.3.7)
- 6 DIO Wafer (CN27, see Chapter 4.3.7)
- 7 M.2 Fixing Bolt for Key M Type 2280

4.3.1. DDR5 SO-DIMM Memory Socket

The KBox N-120-ADN provides one 262-pin DDR5 SO-DIMM socket (Figure 4, pos. 1) to install a memory RAM.

4.3.2. SATA Data / Power Connector

The KBox N-120-ADN provides a SATA data connector (Figure 4, pos. 2) along with a SATA power output wafer (Figure 4, pos. 3) to install a 2.5" SATA HDD / SSD. An additional bracket is provided to hold and fix the hard disk.

4.3.3. M.2 Key B Socket

The KBox N-120-ADN reserves a M.2 Key B socket (Figure 4, pos. 4) with the support of PCIe x1, USB 2.0 and UIM interfaces, allowing the expansion with a Type 22x42 / 30x42 / 22x80 M.2 WWAN module or M.2 SSD drive. Installation of a Type 30x52 module is possible with an appropriate adapter bracket. Kontron offers a matching adapter bracket optionally based on individual requests.

4.3.4. M.2 Key E Socket

The KBox N-120-ADN reserves a M.2 Key E socket (Figure 4, pos. 5) with the support of PCIe x1, USB 2.0, UART, PCM and / or CNVi interfaces, allowing the expansion with a Type 22x30 M.2 WLAN (Wi-Fi or CNVi Wi-Fi), Bluetooth or combo module.

4.3.5. M.2 Key M Socket

The KBox N-120-ADN reserves a M.2 Key M socket (Figure 5, pos. 1) with the support of either SATA 3.0 (default) or PCIe x1 (optional) interface, allowing the expansion with a Type 22x80 M.2 SSD drive.

4.3.6. SIM Card Wafer

The baseboard of the KBox N-120-ADN is equipped with a SIM card wafer (Figure 4, pos. 6) to expand a SIM card holder within the system for accommodating a SIM card for WWAN service access in case of no SIM card holder mounted on the M.2 WWAN expansion card. It is connected to UIM signals on the M.2 Key B socket.



To avoid damage to the SIM card, insert the SIM card before you turn the power on and remove the SIM card after you turn the power off.

4.3.7. RS232/422/485 & DIO Wafer

The baseboard of the KBox N-120-ADN is equipped with 4 RS232/422/485 COM wafers, COM1 (Figure 5, pos. 2), COM2 (Figure 5, pos. 3), COM3 (Figure 5, pos. 4) and COM4 (Figure 5, pos. 5) respectively, as well as a DIO wafer (Figure 5, pos. 6). The system reserves a DB9 cutout (Figure 2, pos. 18) on the real I/O panel, allowing the expansion of either a COM port from any of the RS232/422/485 COM wafers or a DIO port from the DIO wafer.

All RS232/422/485 COM wafers support single communication mode on RS485 with only half-duplex configuration.

The COM1 and COM2 wafers support RS232 without hardware flow control.

The DIO wafer supports 8 digital I/O bits.

5/ Accessing Internal Components

This section contains important information that you must read before accessing the internal components. You must follow these procedures properly when installing, removing or handling any board.

It is recommended to expand your system with applicable expansion card(s) before it is installed into an equipment, machine or cabinet. Please consider following instruction when you install (or remove) an expansion card.

Before installing / removing an add-on card, please pay attention to the following information:

CAUTION

Please observe the "General Safety Instructions for IT-Equipment" provided with the system (refer to the chapter 1/) and the installation instructions in this manual.

Only personnel with appropriate qualifications, trainings and authorization are permitted to install and work with the device.

The installation / removal of HDD / SSD and / or expansion cards may only be performed by a qualified person, according to the description in this manual.

Before removing the cover of the device, make sure that the device is turned off and disconnected from the power supply.

Before you upgrade the device with add-on cards, pay attention to the power specifications in chapter 10/ "Technical Specifications" and make sure that the power consumption of the add-on cards does not exceed 5 W per card.



Please follow the safety instructions for components that are sensitive to electrostatic discharge (ESD). Failure to observe this warning notice may result in damage to the device or the latter's components.



Please pay attention to the manufacturer's instructions before installing / removing an add-on card.

5.1. Opening and Closing the Chassis



The KBox N-120-ADN is factory configured to meet customer requirements. Kontron does not recommend opening the system as this may cause damage to internal components.

CAUTION

Before opening the KBox N-120-ADN, the system must be switched off and disconnected from the main power supply. Also, disconnect all peripheral devices from the KBox N-120-ADN. Before you begin, ensure that you have a clean, flat and ESD-safe surface to work on.

For opening and closing the KBox N-120-ADN, please perform the following steps:

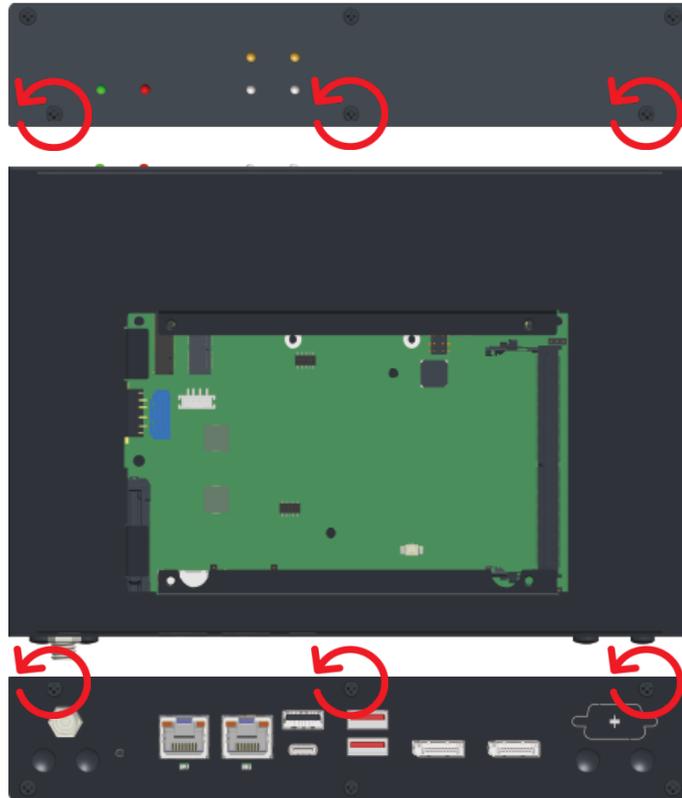
1. Close all applications. Shut down the system properly and disconnect the connection to the main power source. Disconnect all peripherals.
2. The KBox N-120-ADN should lay on a flat, clean surface with the access cover facing upwards.
3. Loosen and remove the Phillips screws (located on the access cover), that secure the access cover to the chassis. Retain the screws for later use.

Figure 6: Unscrewing the access cover



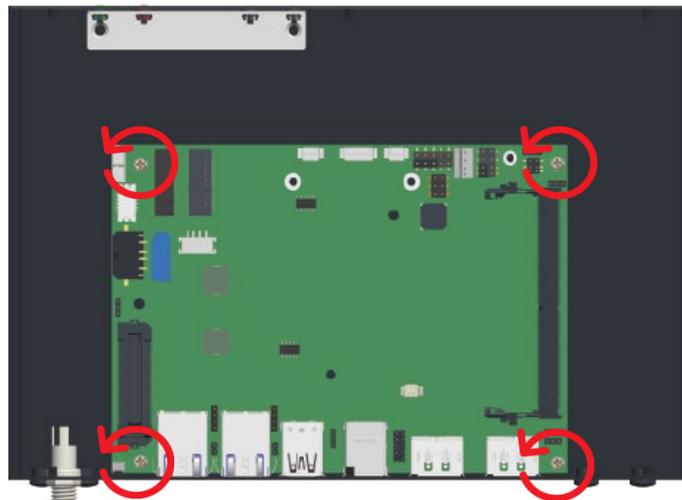
4. Lift the access cover up.
5. Now you have access to the internal DDR5 SO-DIMM, SATA, M.2 Key B and M.2 Key E slots / sockets respectively in order to remove or install DDR5 RAM, 2.5" SATA HDD / SSD, M.2 Key B expansion card / SSD and / or M.2 Key E expansion card.
6. To access the SIM card wafer and antenna holes, loosen and remove the Phillips screws (located on the front I/O panel and the rear I/O panel respectively) that secure the bottom cover to the chassis. Retain the screws for later use.

Figure 7: Unscrewing the bottom cover



7. Lift the bottom cover up.
8. Now you have access to the SIM card wafer and antenna holes in order to remove or install SIM card holder module as well as SIM card and antenna(s).
9. To access the opposite side of the baseboard, loosen and remove the Phillips screws that secure the baseboard cover to the chassis. Retain the screws for later use.

Figure 8: Unscrewing the baseboard



10. Lift the baseboard up and turn it over so that the bottom side is facing up.

11. Now you have access to the internal M.2 Key M socket, RS232/422/485 COM wafers and DIO wafer in order to remove or install a M.2 SSD and / or to draw out a RS232/422/485 or DIO DB9 port on the rear I/O panel.
12. For re-fixing the baseboard replace it carefully to the chassis and screw it on with retained screws. Tighten the retained screws when the baseboard is firmly in place.
13. For re-fixing the bottom cover replace it carefully to the chassis and screw it on with retained screws. Tighten the retained screws when the bottom cover is firmly in place.
14. For closing replace carefully the access cover to the system and screw it on with the retained screws. Tighten the retained screws when the access cover is firmly in place.

NOTICE

When used as intended, the KBox N-120-ADN is to operate only in closed condition. Only when the access cover is properly fixed with the screws and the rear side with WLAN and / or WWAN antenna(s) are properly installed and secured with the screws, it is ensured that the user does not have access to the internal parts of the KBox N-120-ADN.

5.1.1. Installing A SO-DIMM Memory Module

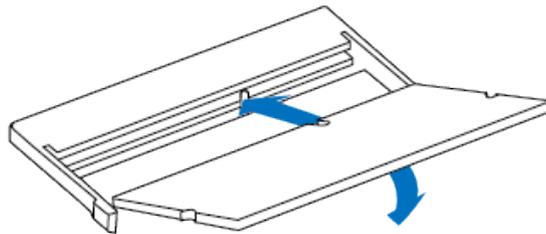
To install a memory module please proceed according to the steps described:

1. Open the device as described in the subsection 5.1 "Opening and Closing the Chassis" (step 1 - 5).
2. Locate the SO-DIMM memory socket (DIMM1) (Figure 4, pos. 1).
3. Align the notch on the SO-DIMM memory module with the notch in the SO-DIMM memory socket.
4. Insert the SO-DIMM memory module into the corresponding socket at a 30-degree angle.
5. Rotate the SO-DIMM memory module down until it snaps into place and the levers close.



Visually check that each lever latch is fully closed and correctly engaged with the notch on the memory module edge.

Figure 9: Installing SO-DIMM Memory Module



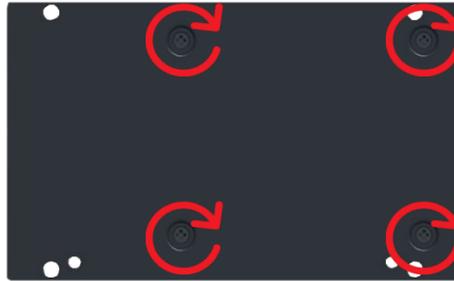
6. In order to close the device, proceed step 14 described in the subsection 5.1 "Opening and Closing the Chassis".

5.1.2. Installing A HDD / SSD

To install a 2.5" HDD / SSD please proceed according to the steps described:

1. Open the device as described in the subsection 5.1 "Opening and Closing the Chassis" (step 1 - 5).
2. Place the 2.5" HDD / SSD on the back side of the access cover removed from the chassis.
3. Align the screw holes of the access cover with those of the HDD / SSD. Secure the HDD / SSD onto the back side of the access cover with the M3 screws.

Figure 10: Screwing 2.5" HDD / SSD



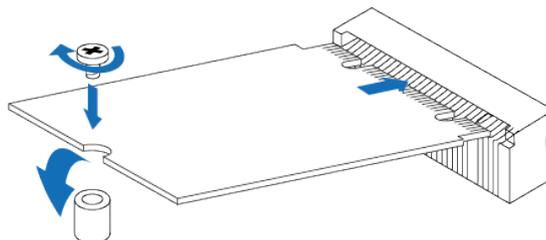
4. Connect the SATA cable to the HDD / SSD.
5. Plug the SATA data cable into SATA data connector (CN10) (Figure 4, pos. 2) and the SATA power cable into SATA power output wafer (CN8) (Figure 4, pos. 3).
6. In order to close the device, proceed step 14 described in the subsection 5.1 "Opening and Closing the Chassis".

5.1.3. Installing A M.2 Key B SSD / Expansion Card and / or A M.2 Key E Expansion Card

To install a M.2 Key B SSD / expansion card and / or a M.2 Key E expansion card please proceed according to the steps described:

1. Open the device as described in the subsection 5.1 "Opening and Closing the Chassis" (step 1 - 5).
2. Locate the M.2 Key B socket (M2B1) (Figure 4, pos. 4) and / or M.2 Key E socket (M2E1) (Figure 4, pos. 5) which you intend to use for installation as well as the corresponding fixing bolt(s) (Figure 4, pos. 7, 8 or both depending on the type of M.2 SSD / expansion card installed into the M.2 Key B socket; Figure 4, pos. 7 for securing the expansion card into the M.2 Key E socket). Depending on the ordered KBox N-120-ADN configuration, Kontron may supply fixing bolt extension(s) and adapter bracket to facilitate the installation of different types of M.2 Key B SSD / expansion card.
3. Align the notch on the M.2 SSD / expansion card with the notch in the M.2 socket (M2B1 / M2E1). Insert the M.2 SSD / expansion card into the corresponding socket (Figure 4, pos. 4 / pos. 5) and rotate it down with the fixing hole of the M.2 SSD / expansion card over the corresponding fixing bolt (Figure 4, pos. 7 / pos. 8).
4. Press the M.2 SSD / expansion card down on the side with the fixing hole and secure it with the available fastening screw as well as fixing bolt extension and / or adapter bracket if necessary.

Figure 11: Installing M.2 SSD / Expansion Card



5. Repeat step 3 and step 4 in case of installing two cards in the M.2 Key B and M.2 Key E sockets respectively. Install the M.2 Key E expansion card first and then stack the M.2 Key B SSD / expansion card on it.
6. In order to close the device, proceed step 14 described in the subsection 5.1 "Opening and Closing the Chassis".

5.1.4. Installing A M.2 Key M SSD

To install an M.2 Key M SSD please proceed according to the steps described:

1. Open the device as described in the subsection 5.1 "Opening and Closing the Chassis" (step 1 - 11).
2. Locate the M.2 Key M socket (M2M1) (Figure 5, pos. 1) and the corresponding fixing bolt (Figure 5, pos. 7).
3. Align the notch on the M.2 SSD with the notch in the M.2 socket. Insert the M.2 SSD into the corresponding socket and rotate it down with the fixing hole of the M.2 SSD over the fixing bolt.
4. Press the M.2 SSD down on the side with the fixing hole and secure it with the available fastening screw.
5. In order to close the device, proceed step 12 - 14 described in the subsection 5.1 "Opening and Closing the Chassis".

5.1.5. Installing A SIM Card

To install a SIM card please proceed according to the steps described:

1. Open the device as described in the subsection 5.1 "Opening and Closing the Chassis" (step 1 - 8).
2. Locate the SIM card wafer (CN6) (Figure 4, pos. 6).
3. Plug the cable connected from SIM card holder module into the SIM card wafer.
4. Secure or place the module in an appropriate location within the chassis.
5. The installation of a SIM card depends on the SIM card holder type. Follow the instructions supplied by the manufacturer to install the SIM card.
6. In order to close the KBox N-120-ADN, proceed step 13 & 14 described in the subsection 5.1 "Opening and Closing the Chassis".

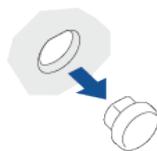
In case of a SIM card holder mounted on the M.2 WWAN expansion card, proceed step 1 - 4 & 6 described in the subsection 5.1.3 "Installing A M.2 Key B SSD / Expansion Card and / or A M.2 Key E Expansion Card" and follow the instructions supplied by the manufacturer to install the SIM card.

5.1.6. Installing Antenna(s)

To install antenna(s) please proceed according to the steps described:

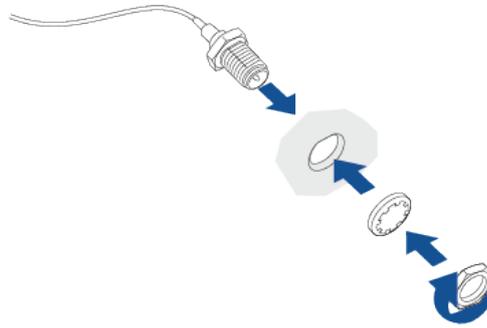
1. Open the device as described in the subsection 5.1 "Opening and Closing the Chassis" (step 1 - 8).
2. Locate the antenna hole (Figure 2, pos. 17).
3. Remove the rubber cap from the antenna hole.

Figure 12: Removing Rubber Cap



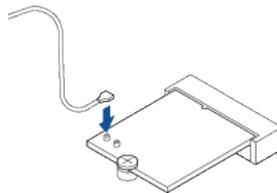
4. Insert the RP-SMA jack of antenna cable into the antenna hole from the inside of the chassis outwards. Ensure that the flat edge of the RP-SMA jack is properly aligned with the flat edge of the hole.
5. Insert the bundled O-ring over the RP-SMA jack.
6. Secure the RP-SMA jack with the bundled hex screw.

Figure 13: Securing RP-SMA Jack



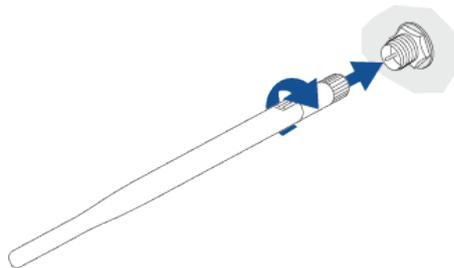
7. Connect the I-PEX end of the antenna cable to the M.2 Key B or Key E expansion card.

Figure 14: Connecting I-PEX End



8. Repeat step 2- 7 to install the second, third and / or fourth antenna cable(s) in case two antennas are required and / or two wireless connections are required.
9. In order to close the KBox N-120-ADN, proceed step 13 & 14 described in the subsection 5.1 "Opening and Closing the Chassis".
10. Screw the external antenna onto its corresponding RP-SMA jack on the rear I/O panel.

Figure 15: Securing External Antenna



11. Repeat step 10 to install the second, third and / or fourth external antenna(s) if any.
12. Position the antenna(s) for optimal signal reception.

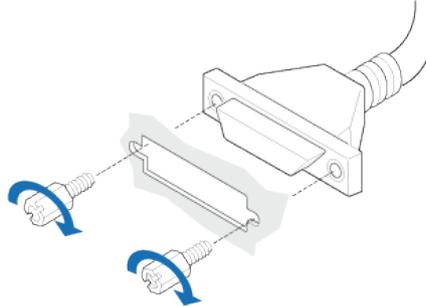
5.1.7. Installing A RS232/422/485 COM Port or DIO Port

To install a RS232/422/485 COM port or a DIO port please proceed according to the steps described:

1. Open the device as described in the subsection 5.1 "Opening and Closing the Chassis" (step 1 - 11).
2. Locate the COM1, COM2, COM3, COM4 or DIO wafer (CN25, CN24, CN28, CN29 or CN27) (Figure 5, pos. 2, 3, 4, 5 or 6).
3. Plug the wafer end of the COM port cable or DIO port cable into the wafer.
4. Use a screwdriver to remove connector knockout for DB9 cutout (Figure 2, pos. 18) on the rear I/O panel.

5. Install the DB9 end of the COM port cable or DIO port cable by inserting it into the DB9 cutout from the back of the rear I/O panel.
6. Secure the DB9 end of the cable on the rear I/O panel with two hex screws.

Figure 16: Installing RS232/422/485 COM Port or DIO Port



7. In order to close the KBox N-120-ADN, proceed step 12 – 14 described in the subsection 5.1 "Opening and Closing the Chassis".

6/ Thermal Considerations

6.1. Available Processors

Please refer to the chapter 10/ "Technical Specifications".



The list of processors may be extended over the product lifetime.

6.2. Convection Cooling

The applied cooling method provides adequate cooling of the device during operation and performs a one-way thermal transfer to the chassis. The top side of the KBox N-120-ADN consist of an aluminum chassis is with cooling fins. The cooling fins provide heat dissipation during operation.



To provide sufficient heat dissipation for the cooling of the KBox N-120-ADN, never cover the cooling fins of the chassis. Do not place any objects on the device.

6.3. System Clearance

To provide a maximum of airflow through and around the box, proper distances to surrounding parts must be observed.

6.4. Maximum Temperatures



The maximum system ambient temperature depends mostly on the power consumption of the processor and the chipset.

For the temperature evaluation a specialised tool from Intel® was used to set the processor to a defined workload. Depending on the power consumption one or more cores were set to 75% workload. This includes the graphics core. The tool also handles the usage of the "Turbo Mode" of certain processor types.



The processor utilization depends highly on the software used. Software using multicore feature will run on several cores whereas standard software will only utilize one core. In this case the processor will use the "Turbo Mode" to increase the clock for the core with the highest workload, as long as the temperature is within limits.

6.5. Third Party Components

When the KBox N-120-ADN is extended and configured with third party components like M.2 expansion card(s) and hard drives (HDD or SSD), it has to be taken into account that the air temperature inside the system is higher than the ambient temperature. An approximately internal temperature rise is given for assistance.

7/ Installation Instructions

The KBox N-120-ADN system is designed for operating:

- ▶ within a control cabinet/ custom enclosure / machine or onto a wall / the back of a monitor by use of a VESA mounting kit
- ▶ in a rack or within a cabinet by use of a rackmount kit
- ▶ as a desktop unit



Expansion card installation should be performed before installing the KBox N-120-ADN into control cabinet / custom enclosure / machine / rack, or onto wall / monitor.

NOTICE

Whenever possible, unpack or pack this product only at EOS/ESD safe work stations. Where a safe work station is not guaranteed, it is important for the user to be electrically discharged before touching the product with his/her hands or tools. This is most easily done by touching a metal part of the system chassis.

Do not handle this product out of its protective enclosure while it is not used for operational purposes unless it is otherwise protected.

Prior any installation work, ensure that there are no live wires on the installation site.

Do not handle the device if there is any damage visible.

Do not operate the KBox N-120-ADN with foreign objects inside the chassis.

Further do not insert any retrieval device into the device while it is connected to power.

Kontron rejects all liability for any and all damages resulting from operation of the unit with foreign objects inside the chassis.

The KBox N-120-ADN has to be installed and operated only by trained and qualified personnel.

Only personnel with appropriate qualifications, trainings and authorization are permitted to install and work with the Kontron KBox N-120-ADN.

This device shall only be installed in or connected to systems that fulfill all necessary technical and specific environmental requirements.

The unit must be placed such that there is sufficient space in front and rear of it for connecting the cables to the I/O interface connectors and for operating the power button.

Leave sufficient free space around the unit to prevent the device from possibly overheating!

Refer also to section 10.1 "Mechanical Specifications".

The KBox N-120-ADN must be firmly attached to a clean flat and solid mounting surface. Use proper fastening materials suitable for the mounting surface. Ensure that the mounting surface type and the used mounting solution safely support the load of the KBox N-120-ADN and the attached components.

Please follow the local/national regulations for grounding.

The voltage feeds must not be overloaded. Adjust the cabling and the overcurrent protection to correspond with the electrical figures indicated on the type label.

The type label is located next to the access cover of the system.

It is recommended that the last cable attached to the system should be the power cable! Refer to the section 7.2 "DC Power Connection" and chapter 8/ "Start Up".

7.1. System Mounting

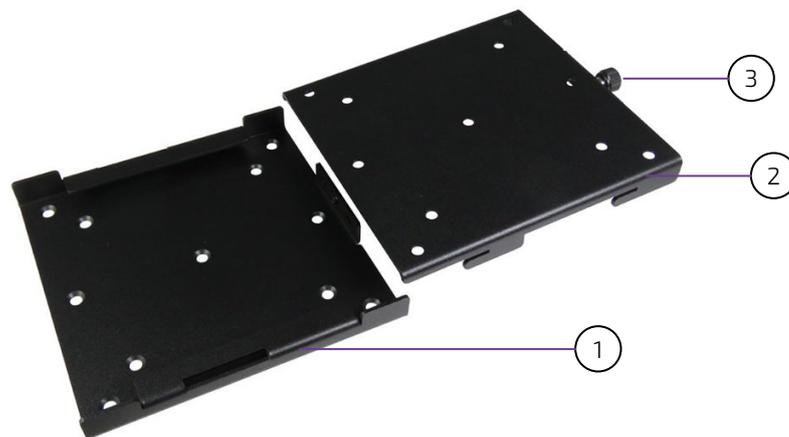
In order to adapt the KBox N-120-ADN for mounting, Kontron offers different mounting solutions such as:

- ▶ KBox N-120-ADN configuration with a VESA mounting kit for vertical / horizontal installation into a control cabinet / custom enclosure / machine / or onto a wall / the back of a monitor
- ▶ KBox N-120-ADN configuration with a rackmount kit for horizontal installation into a rack / control cabinet

7.1.1. VESA Mounting

Depending on the ordered KBox N-120-ADN configuration, your system may be supplied with a VESA mounting kit (Figure 17). The kit consists of two parts: a base bracket (Figure 17, pos. 1) to be fixed permanently on the mounting surface and another hooked bracket (Figure 17, pos. 2) to hold the KBox N-120-ADN with a hand-screw knob (Figure 17, pos. 3) to secure two brackets together.

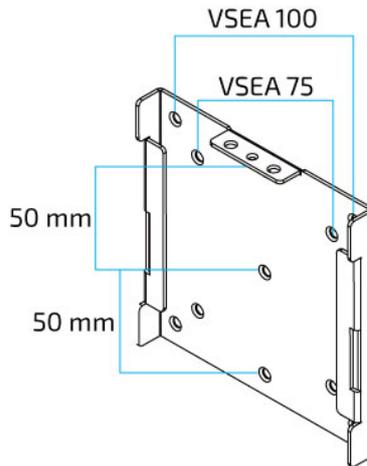
Figure 17: Optional VESA mounting kit



- 1 Base mounting bracket
- 2 Hooked mounting bracket
- 3 Hand-screw knob for securing together base mounting bracket and hooked mounting bracket

The base mounting bracket complies with VESA 75 and VESA 100 patterns (Figure 18). To fasten the bracket, the control cabinet / custom enclosure / machine / monitor / wall must have VESA 75, VESA 100 or other screw pattern shown as Figure 18 for mounting.

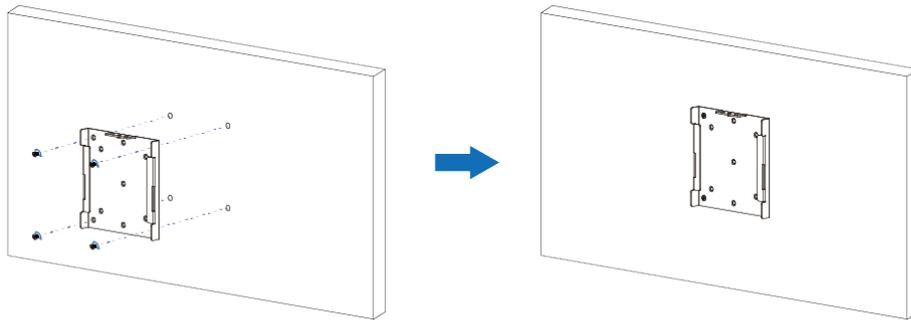
Figure 18: Hole pattern of the base mounting kit



To mount the KBox N-120-ADN please proceed according to the steps described:

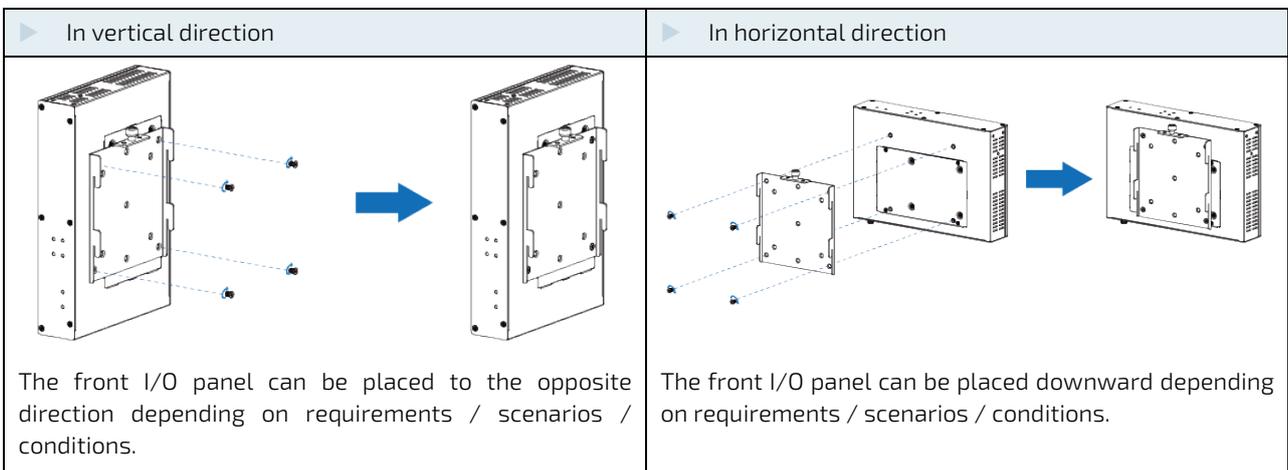
1. Prepare the mounting surface with sufficient screws (four screws for VESA mount) and if necessary anchors corresponding to the mounting surface type if no VESA-compliant screw holes are available. (The recommended screw size is M4 x 6 ~ 10 mm but it still depends on the available screw holes of the mounting surface and never be larger than M4 if any.)
2. Secure the base mounting bracket to the mounting surface with screws (Figure 19).

Figure 19: Securing the base mounting bracket



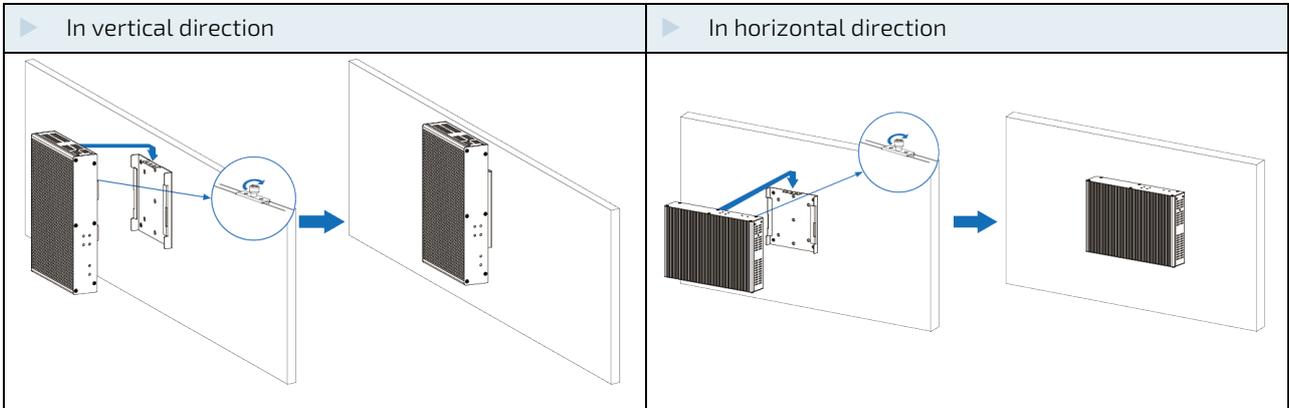
3. Secure the hooked mounting bracket onto the bottom side of the KBox N-120-ADN (Figure 20) with M3 screws (6 mm long) via the VESA 100 mounting holes. The orientation depends on usage requirements, application scenarios and / or surrounding conditions.

Figure 20: Securing the hooked mounting bracket onto the KBox N-120-ADN



4. Place the KBox N-120-ADN onto the mounting surface by sliding the hooked mounting bracket into the based mounting bracket (Figure 21).
5. Secure the hand-screw knob located on the hooked mounting bracket to fix two brackets (Figure 21).

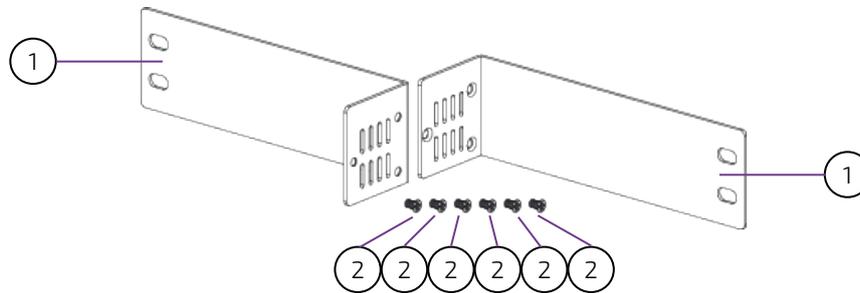
Figure 21: Securing the KBox N-120-ADN onto the mounting surface



7.1.2. Rackmount

Depending on the ordered KBox N-120-ADN configuration, your system may be supplied with a rackmount kit (Figure 22). The kit consists of two rackmount adapter brackets (Figure 22, pos. 1) and six screws (Figure 22, pos. 2).

Figure 22: Optional rackmount kit



- 1 Rackmount adapter bracket
- 2 Screw

To mount the KBox N-120-ADN please proceed according to the steps described:



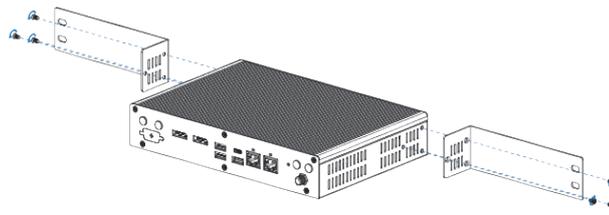
Start installation from the bottom of the rack or rack cabinet.

The heaviest device should be placed in the bottom of the rack or rack cabinet.

Remove the rack door(s) and slide panels if any for easy installation.

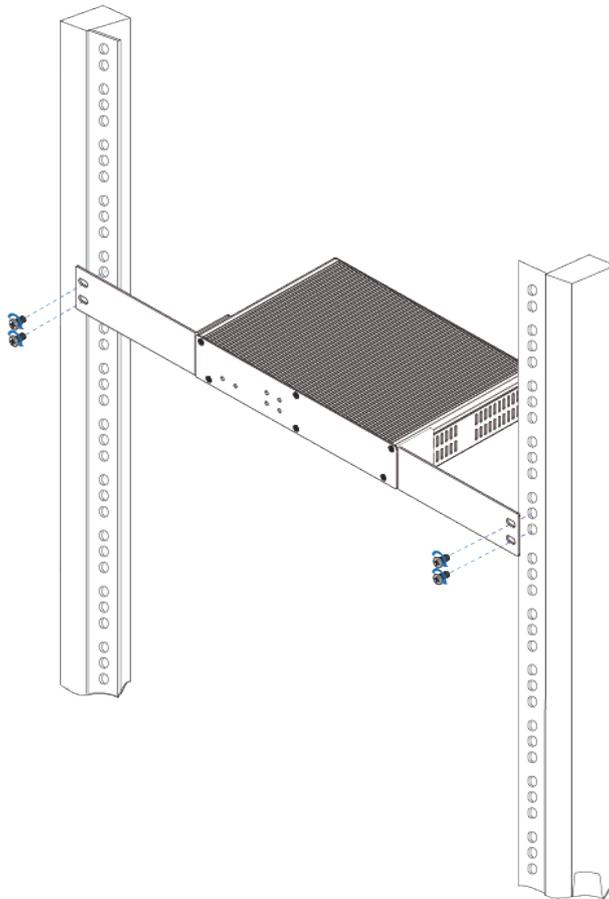
1. Secure the rackmount adapter brackets to the two sides of the KBox N-120-ADN with supplied screws (Figure 23).

Figure 23: Securing rackmount adapter brackets to KBox N-120-ADN



2. Secure the KBox N-120-ADN to the rack or rack cabinet through the installed brackets with the screws that work for your rack or rack cabinet (Figure 24).

Figure 24: Securing KBox N-120-AND to rack or rack cabinet



7.2. DC Power Connection

The KBox N-120-ADN is connected by a lockable DC power input jack (Figure 2, pos. 1) to a DC power source.

Depending on the ordered KBox N-120-ADN configuration, your system may be delivered with a power adapter to convert AC voltage into DC 12 V and a power cord to carry AC power to the power adapter.

8/ Start Up



The KBox N-120-ADN must be only operated with a nominal voltage of 12 V DC of type SELV. For details refer to the chapter 10/ "Technical Specifications".

8.1. Connecting to DC Power Supply

The DC power input jack (Figure 2, pos. 1) is located on the rear side of the KBox N-120-ADN. The KBox N-120-ADN will be connected to a AC-to-DC power adapter via the attached male DC power plug and corresponding power cable connected to the adapter.



Before using your system, become familiar with the system components and check that everything is connected properly. Following a proper cabling procedure will prevent a false power-on condition, which could result in unit operational failure.

When you install/disconnect the unit, the functional earth connection must always be made first and disconnected last.

Also, it is recommended that the last connections attached to the system should be the power wires!

NOTICE

The KBox N-120-ADN must be connected DC main power supply complying with the SELV (Safety Extra Low Voltage) requirements of EN 60950-1 standard. It must be observed that wiring and short-circuit/overcurrent protection is performed according to the applicable standards, regulations and respect to the electrical specification of the KBox N-120-ADN.

Even when the system is turned off via the power button (Figure 2, pos. 2) parts of the system are still energized.

The disconnecting device (fuse/circuit breaker) rating must be in accordance with the wire cross-section and the rated current of the KBox N-120-ADN.



The cables must have some form of support so as to minimize the strain on the unit's connectors.

To connect the KBox N-120-ADN to a corresponding DC power supply, please perform the following steps:

1. Connect the power adapter cable to the DC jack (DC IN, Figure 2, pos. 1) of the KBox N-120-ADN.
2. Connect the power cable to the power adapter.
3. Connect the other end of the power cable to a power outlet.

8.2. Operating System and Hardware Component Drivers

Your system can be supplied optionally with a pre-installed operating system.

If you have ordered your KBox N-120-ADN with a pre-installed operating system, all drivers are installed in accordance with the system configuration ordered (optional hardware components). Your system is fully operational when you switch it on for the first time.

If you have ordered the KBox N-120-ADN without a pre-installed operating system, you will need to install the operating system and the appropriate drivers for the system configuration you have ordered (optional hardware components) yourself.



You can download the relevant drivers for the installed hardware from our web site at [www.kontron.com](https://customersection.kontron.com) in Customer Section (<https://customersection.kontron.com>) by selecting the product.

Pay attention to the manufacturer specifications of the operating system and the integrated hardware components.

9/ Maintenance and Cleaning

Equipment from Kontron requires only minimum servicing and maintenance for proper operation.

- ▶ For light soiling, clean the KBox N-120-ADN with a dry cloth. Carefully remove dust from the surface of the chassis using a clean, soft brush.
- ▶ Stubborn dirt should be removed using a mild detergent and a soft cloth.



Do not use steel wool, metallic threads or solvents like abrasives, alcohol, acetone or benzene for cleaning the KBox N-120-ADN.

10/ Technical Specifications

Table 4: Technical Specifications

System	
Processor	<ul style="list-style-type: none"> ▶ Intel® Atom® x7000E Series Processors ▶ Intel® Core™ i3 N-Series Processors ▶ Intel® N-Series Processors
Memory	▶ 1x DDR5 SO-DIMM
Video	
Display Interface	▶ 3x DP (4096 x 2160 @ 60 Hz, 2x Full-size DP on rear, 1x DP USB-C on rear)
Network Connection	
Ethernet	▶ 2x 2.5 GbE LAN (RJ45 on rear, Intel® I226-V)
Peripheral Connection	
USB	<ul style="list-style-type: none"> ▶ 3x USB 3.2 Gen 2 (2x Type A on rear, 1x Type C (w/ DP & PD 5 V / 3 A) on rear) ▶ 1x USB 2.0 (Type A on rear)
Other I/Os	▶ 1x RS232/422/485 (optional, DB9 on rear) or DIO (optional, 8-bit, DB9 Female on rear)
Storage & Expansion	
Storage & Expansion	<ul style="list-style-type: none"> ▶ 1x 2.5" SATA 3.0 HDD / SSD support ▶ 1x M.2 Key B (Type 2242 / 3042 / 3052 / 2280, with PCIe x1 / USB 2.0 / UIM) ▶ 1x M.2 Key E (Type 2230, with PCIe x1 / USB 2.0 / CNVi) ▶ 1x M.2 Key M (Type 2280, with SATA 3.0 (default) / PCIe x1 (optional)) ▶ 1x SIM Card Holder (optional, by header)
Power	
Input Voltage	▶ DC 12 V
Connector	▶ Lockable DC Jack (on rear)
Firmware	
BIOS	▶ AMI uEFI BIOS w/ 256 Mb SPI Flash
Watchdog	▶ Programmable WDT to generate system reset event
H/W Monitor	<ul style="list-style-type: none"> ▶ Voltages ▶ Temperatures
Real Time Clock	▶ Processor integrated RTC
Security	▶ TPM 2.0 support (Infineon SLB 9672)
System Control & Monitoring	
Button, Switch & Indicator	<ul style="list-style-type: none"> ▶ 1x Power Button (on rear) ▶ 2x Power LED (1x on front, 1x on rear) ▶ 1x Standby LED (on rear) ▶ 1x M.2 Key M Storage LED (on front) ▶ 2x LAN Act / Link LED (on front, orange) ▶ 2x LAN Speed LED (on front, 10/100: off / 1000: yellow / 2.5G: green)
Cooling	

System	
Cooling Method	▶ Passive
Software	
OS Support	▶ Windows 11, Windows 10, Linux

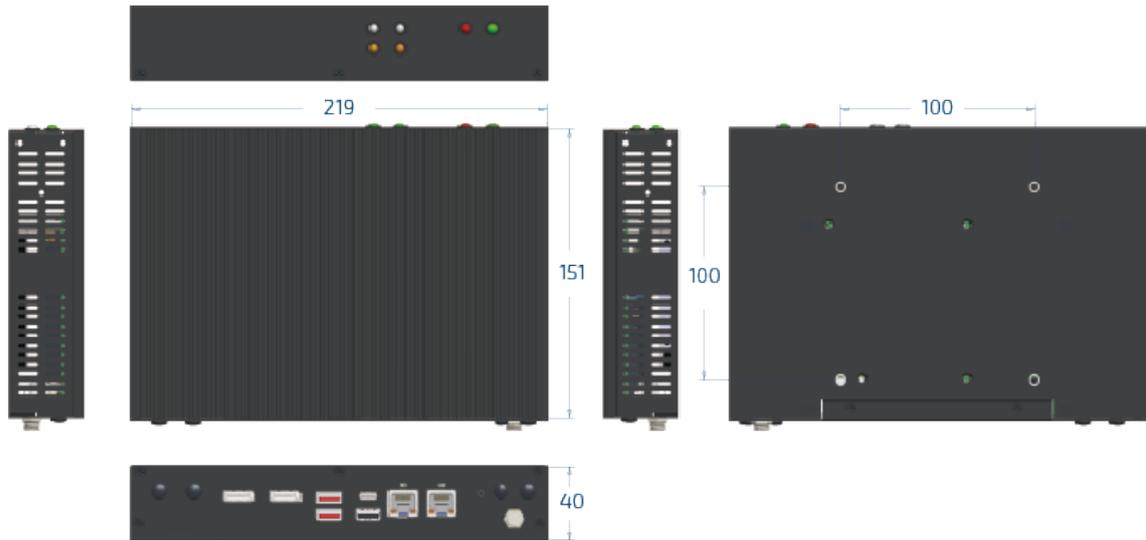
10.1. Mechanical Specifications

Table 5: Mechanical Specifications

Construction	▶ Aluminum + Metal Chassis
Dimensions (W x D x H)	▶ 219 mm x 151 mm x 40 mm / 8.62" x 5.94" x 1.57"
Weight	▶ 1,420 g / 3.13 lb
Mounting	▶ VESA Mount (100 mm x 100 mm) ▶ Rackmount ▶ Desktop

10.1.1. Mechanical Drawing

Figure 25: Mechanical Drawing



(unit: mm)

10.2. Environmental Conditions

Table 6: Environmental Conditions

Operating Temperature	▶ 0 °C ~ 50 °C / 32 °F ~ 122 °F
Storage Temperature	▶ -20 °C ~ 80 °C / -4 °F ~ 176 °F
Humidity	▶ 0 % ~ 95 %

10.3. Standards and Certifications

Table 7: Standards and Certifications

CE / UKCA Class B	▶ TBD
FCC / ICES Class B	▶ TBD

11/Standard Interfaces – Pin Assignments

Low-active signals are indicated by a minus sign.

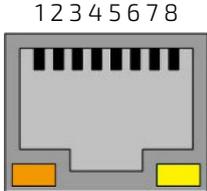
11.1. DC Jack

Table 8: DC Jack (see Figure 2, pos. 1)

Pin	Signal Name	DC Jack (female)
1	+12V DC	
2	GND	

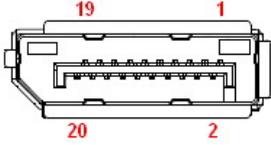
11.2. 2.5 Gigabit Ethernet Connector

Table 9: 2.5 Gigabit Ethernet Connector (see Figure 2, pos. 3 & 4)

Pin	Signal Name	RJ45
1	TX1+	
2	TX1-	
3	TX2+	
4	TX3+	
5	TX3-	
6	TX2-	
7	TX4+	
8	TX4-	

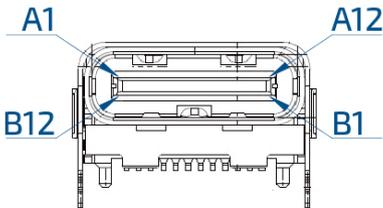
11.3. DP Connector

Table 10: DP Connector (see Figure 2, pos. 5)

Pin	Signal Name	DP Connector (Full Size)
1	ML_Lane0p	
2	GND	
3	ML_Lane0n	
4	ML_Lane1p	
5	GND	
6	ML_Lane1n	
7	ML_Lane2p	
8	GND	
9	ML_Lane2n	
10	ML_Lane3p	
11	GND	
12	ML_Lane3n	
13	Config1	
14	Config2	
15	AUX_CHp	
16	GND	
17	AUX_CHn	
18	Hot_Plug	
19	GND	
20	DP_PWR	

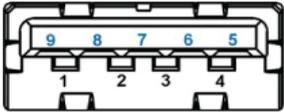
11.4. DP over USB Type C Connector

Table 11: DP over USB Type C Connector (see Figure 2, pos. 6)

Pin	Signal Name	DP over USB Type C Connector (USB-C Version 3.2 Gen 2 / 2.0)
A1	GND	 <p>The diagram shows a cross-section of a USB Type-C connector. Four pins are highlighted with blue circles and labels: A1 (top left), A12 (top right), B1 (bottom right), and B12 (bottom left). The connector is shown in a perspective view, revealing the internal contacts and the surrounding housing.</p>
A2	CON_TX1P_C	
A3	CON_TX1N_C	
A4	+5V_VBUS	
A5	CC1	
A6	USB2_P	
A7	USB2_N	
A8	SBU1	
A9	+5V_VBUS	
A10	CON_RX2N_C	
A11	CON_RX2P_C	
A12	GND	
B1	GND	
B2	CON_TX2P_C	
B3	CON_TX2N_C	
B4	+5V_VBUS	
B5	CC2	
B6	USB2_P	
B7	USB2_N	
B8	SUB2	
B9	+5V_VBUS	
B10	CON_RX1N_C	
B11	CON_RX1P_C	
B12	GND	

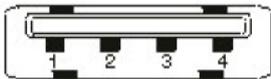
11.5. USB 3.2 Gen 2 Port

Table 12: USB 3.2 Gen 2 Port (see Figure 2, pos. 7)

USB 2.0 Contact Pin		USB 3.2 Gen 2 Contact Pin		9-pin USB Connector Type A Version 3.2 Gen 2 / 2.0
Pin	Signal Name	Pin	Signal Name	
1	+USB_VCC	5	USB_RX-	
2	USB_D-	6	USB_RX+	
3	USB_D+	7	GND	
4	GND	8	USB_TX-	
		9	USB_TX+	

11.6. USB 2.0 Port

Table 13: USB 2.0 Port (see Figure 2, pos. 8)

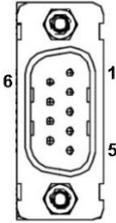
Pin	Signal Name	4-pin USB Connector Type A Version 2.0
1	+USB_VCC	
2	USB_D-	
3	USB_D+	
4	GND	

11.7. RS232/422/485 Serial Port or DIO Port

The rear I/O panel reserves a DB9 cutout for additional support of a RS232/422/485 serial port or an 8-bit digital I/O port.

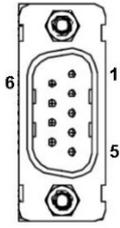
- ▶ in case of connecting to COM3 (Figure 5, pos. 4) or COM4 (Figure 5, pos. 5) wafer, refer to Table 14
- ▶ in case of connecting to COM1 (Figure 5, pos. 2) or COM2 (Figure 5, pos. 3) wafer, refer to Table 15
- ▶ in case of connecting to DIO wafer (Figure 5, pos. 6), refer to Table 16

Table 14: RS232/422/485 Serial Port (see Figure 2, pos. 18)

Pin	RS232	RS422	RS485	9-pin D-SUB Male Connector	
1	DCD	TX-	DATA-		
2	RXD	TX+	DATA+		
3	TXD	RX+	N/A		
4	DTR	RX-	N/A		
5	GND	GND	GND		
6	DSR	N/A	N/A		
7	RTS	N/A	N/A		
8	CTS	N/A	N/A		
9	RI*	N/A*	N/A*		

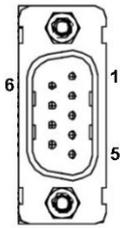
* Kontron's default setting. Users can alternatively connect it to Pin 10 in the wafer for +5V signal.

Table 15: RS232/422/485 Serial Port without Hardware Flow Control (see Figure 2, pos. 18)

Pin	RS232	RS422	RS485	9-pin D-SUB Male Connector
1	N/A	TX-	DATA-	
2	RXD	TX+	DATA+	
3	TXD	RX+	N/A	
4	N/A	RX-	N/A	
5	GND	GND	GND	
6	N/A	N/A	N/A	
7	N/A	N/A	N/A	
8	N/A	N/A	N/A	
9	N/A*	N/A*	N/A*	

* Kontron's default setting. Users can alternatively connect it to Pin 10 in the wafer for +5V signal.

Table 16: Digital I/O Port (see Figure 2, pos. 18)

Pin	Signal Name	9-pin D-SUB Female Connector
1	DIO_0	
2	DIO_1	
3	DIO_2	
4	DIO_3	
5	DIO_4	
6	DIO_5	
7	DIO_6	
8	DIO_7	
9	+5V	

12/ BIOS

12.1. Starting the uEFI BIOS

The KBox N-120-ADN is provided with a Kontron-customized, pre-installed and configured version of AMI Aptio® V uEFI BIOS. AMI BIOS firmware is based on the Unified Extensible Firmware Interface (UEFI) specification and the Intel® Platform Innovation Framework for EFI. This uEFI BIOS provides a variety of new and enhanced functions specifically tailored to the hardware features of the KBox N-120-ADN.

The uEFI BIOS comes with a setup program that provides quick and easy access to the individual function settings for control or modification of the uEFI BIOS configuration. The setup program allows the accessing of various menus that provide functions or access to sub-menus with more specific functions of their own.

To start the uEFI BIOS setup program, follow the steps below:

1. Power on the board.
2. Wait until the first characters appear on the screen (POST messages or splash screen).
3. Press the key.
4. If the uEFI BIOS is password-protected, a request for password will appear. Enter either the User Password or the Supervisor Password (see Security menu), press <ENTER>, and proceed with step 5.
5. A setup menu will appear.

The KBox N-120-ADN uEFI BIOS setup program uses a hot key-based navigation system. A hot key legend bar is located on the bottom of the setup screens.

The following table provides information concerning the usage of these hot keys.

Table 17: Hotkeys Table

Signal	Description
<F1>	The <F1> key invokes the General Help window.
<->	The <Minus> key selects the next lower value within a field.
<+>	The <Plus> key selects the next higher value within a field.
<F2>	The <F2> key loads the previous values.
<F3>	The <F3> key loads the standard default values.
<F4>	The <F4> key saves the current settings and exit the uEFI BIOS setup.
<→> or <←>	The <Left/Right> arrows select major setup menus on the menu bar. For example: Main, Advanced, Security, etc.
<↑> or <↓>	The <Up/Down> arrows select fields in the current menu. For example: A setup function or a sub-screen.
<ESC>	The <ESC> key exits a major setup menu and enter the Exit setup menu. Pressing the <ESC> key in a sub-menu displays the next higher menu level.
<ENTER>	The <ENTER> key executes a command or select a submenu.

12.2. Starting the uEFI BIOS

The Setup utility features shows six menus in the selection bar at the top of the screen:

- ▶ Main
- ▶ Advanced
- ▶ Chipset
- ▶ Security
- ▶ Boot
- ▶ Save & Exit

The Setup menus are selected via the left and right arrow keys. The currently active menu and the currently active uEFI BIOS Setup item are highlighted in white. Each Setup menu provides two main frames. The left frame displays all available functions. Functions that can be configured are displayed in blue. Functions displayed in gray provide information about the status or the operational configuration. The right frame displays an Item Specific Help window providing an explanation of the respective function.

12.2.1. Main Setup Menu

Upon entering the uEFI BIOS Setup program, the Main Setup menu is displayed. This screen lists the Main Setup menu sub-screens and provides basic system information. Additionally functions for setting the system time and date are offered.

Table 18: Main Setup Menu Sub-Screens and Functions

Function	Description
Product Information	Read only field. Displays information about the product name
BIOS Information	Read only field. Displays information about the system BIOS
FSP Information	Read only field. Displays information about the FSP
Board Information	Read only field. Displays information about the baseboard
Processor Information	Read only field. Displays information about the processor
Memory Information	Read only field. Displays information about the memory
PCH Information	Read only field. Displays information about the PCH
ME Information	Read only field. Displays information about Intel® Management Engine (ME) firmware
System Language	Read only field. [English] only
Platform Information	Sub-screen to the product and KSC information.
System Date	Set System Date
System Time	Set System Time

Figure 26: BIOS Main Menu Screen System Data and Time

Aptio Setup – AMI					
Main	Advanced	Chipset	Security	Boot	Save & Exit
Product Information					
Product Name	KBox N-120-ADN				
BIOS Information					
BIOS Vendor	American Megatrends				
Core Version	5.27				
Compliance	UEFI 2.8; PI 1.7				
Kontron BIOS Version	KE120ADN.110 (x64)				
Access Level	Administrator				
FSP Information					
FSP Version	0C.02.89.40				
RC Version	0C.E0.89.40				
Build Date					
FSP Mode	Dispatch Mode				
Board Information					
Board Name	KBox N-120-ADN				
Board I/D	N/A				
Fab ID	Default string				
LAN PHY Revision	N/A				
Processor Information					
Name	Alder Lake ULX				
Type	Intel® Core™ i3-N305				
Speed	1800 MHz				
ID	0xB06E0				
Stepping	A0				
Package	Not Implemented Yet				
Number of Efficient-cores	8 Core(s) / 8 Thread(s)				
Microcode Revision	17				
GT Info	0x46D0				
eDRAM Size	N/A				
IGFX GOP Version	21.0.1063				
Memory RC Version	0.0.4.74				
Total Memory	8192 MB				
Memory Frequency	4800 MHz				
PCH Information					
Name	PCH-N				

Aptio Setup – AMI					
Main	Advanced	Chipset	Security	Boot	Save & Exit
PCH SKU		N Premium SKU			
Stepping		A0			
Chipset Init Base Revision		4			
Chipset Init OEM Revision		0			
Package		Not Implemented Yet			
TXT Capability of Platform / PCH		Unsupported			
Production Type		Production			
Dual Output Fast Read support		Supported			
Read ID / Status Clock Freq		50 MHz			
Write and Erase Clock Freq		50 MHz			
Fast Read Clock Freq		50 MHz			
Fast Read support		Supported			
Number of Components		1 Component			
SPI Component 0 Density		32 MB			
eSPI Flash Sharing Mode		G3			
EC PECl Mode		Legacy PECl mode			
ME FW Version		16.50.12.1453			
ME Firmware SKU		Consumer SKU			
PMC FW Version		160.50.0.1010			
System Language		[English]			
> Platform Information					
System Date		[Tue 05/01/2025]			
System Time		[18:57:58]			
→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit					
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Feature	Option	Description
System Date	[dd/mm/yyyy]	Set the Date. Use Tab to switch between Data elements. Default Range: Year: 1998 – 9999 Months: 1 – 12 Days: Dependent on month Range of Years may vary.
System Time	[hh:mm:ss]	Set the Time. Use Tab to switch between Time elements.

Figure 27: BIOS Main Menu Screen – Platform Information

Aptio Setup – AMI		
Main		
Board Information		
Product Name	KBox N-120-ADN	
Serial #	Default string	
UUID	00020003-0004-0005-0006-000700080009	
KSC Information		
Controller	KSC Main Controller	
Operating Mode	Normal	
Board Name	3.5-ADN_AML	
Platform ID	000A	→ ←: Select Screen
KSC SW Spec. Version	1.20	↑ ↓: Select Item
BIOS Protocol Version	2.3.1	Enter: Select
BIOS SW Spec. Version	1.18	+/-: Change Opt.
Core Firmware Version	1.4.1 Release	F1: General Help
Board Firmware Version	0.1.0 Release	F2: Previous Values
SCM Info	E9-AC-08-91	F3: Optimized Defaults
Boot Counter	N/A	F4: Save & Exit
		ESC: Exit
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12.2.2. Advanced Setup Menu

The Advanced setup menu provides sub-screens and functions for advanced configurations. The following sub-screen functions are included in the menu:

- ▶ IBEC, Compliance Test, Audio, Power Mode, ME FW Image Re-Flash & Intel® TCC Mode Configuration
- ▶ Display Configuration
- ▶ Trusted Computing
- ▶ ACPI Settings
- ▶ Miscellaneous
- ▶ H/W Monitor
- ▶ S5 RTC Wake Settings
- ▶ Serial Port Console Redirection
- ▶ AMI Graphic Output Protocol Policy
- ▶ SIO Configuration
- ▶ USB Configuration
- ▶ Network Stack Configuration
- ▶ NVMe Configuration
- ▶ F81435 Configuration
- ▶ Intel® Ethernet Controller - I226-V -C0:EA:C3:D1:D0: CA
- ▶ Intel® Ethernet Controller - I226-V -C0:EA:C3:D1:D0: CB

NOTICE

Setting items on this screen to incorrect values may cause the system to malfunction.

Figure 28: BIOS Advanced Menu

Aptio Setup – AMI					
Main	Advanced	Chipset	Security	Boot	Save & Exit
In-Band ECC Support			[Disabled]		
Compliance Test Mode			[Disabled]		
HD Audio			[Enabled]		
Power Mode Selection			[AT Mode]		
ME FW Image Re-Flash			[Disabled]		
Intel® TCC Mode			[Disabled]		
> Display Configuration					
> Trusted Computing					
> ACPI Settings					
> Miscellaneous					
> H/W Monitor					
> S5 RTC Wake Settings					
> Serial Port Console Redirection					
> AMI Graphic Output Protocol Policy					
> SIO Configuration					
> USB Configuration					
> Network Stack Configuration					
> NVMe Configuration					
> F81435 Configurations					
> Intel® Ethernet Controller I226-V – C0:EA:C3:D1:D0:CA					
> Intel® Ethernet Controller I226-V – C0:EA:C3:D1:D0:CB					
				→ ←: Select Screen	
				↑ ↓: Select Item	
				Enter: Select	
				+/-: Change Opt.	
				F1: General Help	
				F2: Previous Values	
				F3: Optimized Defaults	
				F4: Save & Exit	
				ESC: Exit	
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Feature	Option	Description
In-Band ECC Support	[Disabled], [Enabled]	Enable / Disable In-Band ECC. Will be enabled if memory has symmetric configuration
Compliance Test Mode	[Disabled], [Enabled]	Enable when using Compliance Load Board
HD Audio	[Disabled], [Enabled]	Control Detection of the HD-Audio device. [Disabled] = HDA will be unconditionally disabled. [Enabled] = HDA will be unconditionally enabled.
Power Mode Selection	[AT mode]	Read only item.
ME FW Image Re-Flash	[Disabled], [Enabled]	Enable / Disable ME FW Image Re-Flash function.
Intel® TCC Mode	[Disabled], [Enabled]	Enable or Disable Intel® TCC Mode. When enabled, this will modify system settings to improve real-time performance. The full list of settings and their current state are displayed below when Intel® TCC mode is enabled.

Figure 29: BIOS Advanced Menu - Display Configuration

Aptio Setup – AMI		
Advanced		
Display Configuration		
VBT Select	[DP]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Primary Display	[IGFX]	
Internal Graphics	[Enabled]	
Aperture Size	[256MB]	
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* These items appear only when enabling Active LVDS.

(1) These items appear only when selecting By External for PWM Backlight Control.

(2) This item appears only when selecting PWM for the LVDS Backlight Control Mode.

(3) This item appears only when selecting Voltage for the LVDS Backlight Control Mode.

Feature	Option	Description
VBT Select	[DP], [HDMI]	Select VBT for GOP Driver Select VBT to MIPI if any of Display has MIPI
Primary Display	[Auto], [IGFX], [PEG Slot], [PCH PCI]	Select which of IGFX / PEG / PCI Graphics device should be Primary Display or select HG for Hybrid Gfx.
Internal Graphics	[Enabled]	Read only item
Aperture Size	[128MB], [256MB], [512MB], [1024MB]	Select the Aperture Size. Note: Above 4GB MMIO BIOS assignment is automatically enabled when selecting > 2048MB aperture. To use this feature, please disable CSM Support.

Figure 30: BIOS Advanced Menu – Trusted Computing

Aptio Setup – AMI		
Advanced		
TPM 2.0 Device Found		
Firmware Version:	16.13	
Vendor:	IFX	
Security Device Support	[Enabled]	
Active PCR banks*	SHA256	
Available PCR banks*	SHA256, SHA384	
SHA256 PCR Bank*	[Enabled]	
SHA384 PCR Bank*	[Disabled]	
Pending operation*	[None]	
Platform Hierarchy*	[Enabled]	
Storage Hierarchy*	[Enabled]	
Endorsement Hierarchy*	[Enabled]	
Physical Presence Spec Version*	[1.3]	
TPM 2.0 Interface Type*	[TIS]	
Device Select*	[Auto]	
		→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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* These items appear only when enabling Security Device Support.

Feature	Option	Description
Security Device Support	[Disabled], [Enabled]	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.
SHA256 PCR Bank	[Disabled], [Enabled]	Enable or Disable SHA256 PCR Bank
SHA384 PCR Bank	[Disabled], [Enabled]	Enable or Disable SHA384 PCR Bank
Pending operation	[None], [TPM Clear]	Schedule an Operation for the Security Device. NOTE: Your Computer will reboot during restart in order to change State of Security Device.
Platform Hierarchy	[Disabled], [Enabled]	Enable or Disable Platform Hierarchy
Storage Hierarchy	[Disabled], [Enabled]	Enable or Disable Storage Hierarchy
Endorsement Hierarchy	[Disabled], [Enabled]	Enable or Disable Endorsement Hierarchy
Physical Presence Spec Version	[1.2], [1.3]	Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.
TPM 2.0 Interface Type	[TIS]	Read only item

Feature	Option	Description
Device Select	[TPM 1.2], [TPM 2.0], [Auto]	TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with the default set to TPM 2.0 devices if not found, TPM 1.2 devices will be enumerated.

Figure 31: BIOS Advanced Menu –ACPI Settings

Aptio Setup – AMI		
Advanced		
ACPI Settings		
Enable ACPI Auto Configuration	[Disabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Enable Hibernation*	[Enabled]	
ACPI Sleep State*	[S3 (Suspend to RAM)]	
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* These items appear only when disabling Enable ACPI Auto Configuration.

Feature	Option	Description
Enable ACPI Auto Configuration	[Disabled], [Enabled]	Enables or Disables BIOS ACPI Auto Configuration.
Enable Hibernation	[Disabled], [Enabled]	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some operating systems.
ACPI Sleep State	[Suspend Disabled], [S3 (Suspend to RAM)]	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.

Figure 32: BIOS Advanced Menu – Miscellaneous

Aptio Setup – AMI		
Advanced		
Miscellaneous Configuration		
> Preset DIO in BIOS > Control KSC firmware > Update KSC firmware > Generic eSPI Decode Ranges > Watchdog		
Reset Button Behavior	[Chipset Reset]	
I2C Speed	[100 KHz]	→ ←: Select Screen
Onboard I2C Mode	[Multimaster]	↑ ↓: Select Item
Manufacturing mode	[Disabled]	Enter: Select
BIOS Test Mode	[Disabled]	+/-: Change Opt.
Last system reset through	[Power-on reset]	F1: General Help
Create GSPI ACPI dev	[Disabled]	F2: Previous Values
PCIe Wake	[Disabled]	F3: Optimized Defaults
Onboard EEPROM Write Protect	[WP Enabled]	F4: Save & Exit
		ESC: Exit
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Feature	Option	Description
Reset Button Behavior	[Chipset Reset], [Power Cycle]	Select Reset Button Behavior: Chipset Reset & Power Cycle.
I2C Speed	[100 KHz], [400 KHz], [1 MHz]	Select I2C Bus Speed in KHz. For a default system 100 KHz should be an appropriate value.
Onboard I2C Mode	[Multimaster], [Busclear]	MultiMaster / BusClear
Manufacturing mode	[Disabled]	Read only item
BIOS Test Mode	[Disabled]	Read only item
Last system reset through	[Power-on reset]	Read only item
Create GSPI ACPI dev	[Disabled], [Kontron Linux BSP], [Win10 RhProxy style]	If set to 'Kontron Linux BSP' then a generic GSPI device will be created in ACPI space to be used by Kontron Linux BSP. 'Win10 RhProxy style' supports this driver type under Win10.
PCIe Wake	[Disabled], [Enabled]	Set to enable or disable PCIe wake. This would affect features such as Wake O/1 and Wake from Lan (WOL).
Onboard EEPROM Write Protect	[WP Disabled], [WP Enabled]	Set WP enable or disable the Onboard EEPROM Write Protect

Figure 33: BIOS Advanced Menu – Miscellaneous – Preset in BIOS

Aptio Setup – AMI		
Advanced		
Allows to preset GPIOs during BIOS startup.		
GPIO OS usable	[GPIO 0 – GPIO 7]	
Control DIO in BIOS	[Disabled]	
DIO #0*	[Skip]	
Output level ^{*(1)}	[Low]	
DIO #1*	[Skip]	
Output level ^{*(1)}	[Low]	
DIO #2*	[Skip]	
Output level ^{*(1)}	[Low]	
DIO #3*	[Skip]	
Output level ^{*(1)}	[Low]	
DIO #4*	[Skip]	
Output level ^{*(1)}	[Low]	
DIO #5*	[Skip]	
Output level ^{*(1)}	[Low]	
DIO #6*	[Skip]	
Output level ^{*(1)}	[Low]	
DIO #7*	[Skip]	
Output level ^{*(1)}	[Low]	
		→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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* These items appear only when enabling Control DIO in BIOS.

⁽¹⁾ This item appears only when selecting Output for DIO #0/1/2/3/4/5/6/7 respectively.

Feature	Option	Description
GPIO OS usable	[All available GPIO], [GPIO 0 – GPIO 7]	Set the GPIO OS usable
Control DIO in BIOS	[Disabled], [Enabled]	Enables or disables DIO GPIO control in BIOS. If set to 'disabled' then the GPIOs are not touched by BIOS.
DIO #0..7	[Input], [Output], [Skip]	Determine the type of the DIO configuration. If this is set to 'Skip' then this GPIO will be left untouched.
Output level	[Low], [High]	Set the level of a DIO pin

Figure 34: BIOS Advanced Menu – Miscellaneous – Control KSC firmware

Aptio Setup – AMI	
Advanced	
Allows to control KSC firmware related settings.	

Aptio Setup – AMI		
Advanced		
Lock FW update access	[Enabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
> KSC OTP area control		
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Feature	Option	Description
Lock FW update access	[Disabled], [Enabled]	Locks access to KSC firmware area during runtime.

Figure 35: BIOS Advanced Menu – Miscellaneous – Control KSC firmware –KSC OTP area control

Aptio Setup – AMI		
Advanced		
Allows to control KSC OTP area related settings.		
KSC OTP access lock	[Enabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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Feature	Option	Description
KSC OTP access lock	[Enabled]	Read only item

Figure 36: BIOS Advanced Menu – Miscellaneous – Update KSC firmware

Aptio Setup – AMI		
Advanced		
Allows to update KSC firmware from BIOS.		
Auto update KSC FW	[Disabled]	→ ←: Select Screen

Aptio Setup – AMI	
Advanced	
	↑ ↓ : Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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Feature	Option	Description
Auto update KSC FW	[Disabled], [Enabled]	Updates KSC firmware to BIOS internal version (best known config) on next system start. To update FW set item to 'Enabled' and exit the setup using 'Save changes and exit'.

Figure 37: BIOS Advanced Menu – Miscellaneous – Generic eSPI Decode Ranges

Aptio Setup – AMI	
Advanced	
Generic eSPI Decode Ranges	
Generic LPC via eSPI Decode 1	[Disabled]
Base Address*	100
Length*	8
	→ ←: Select Screen ↑ ↓ : Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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* These items appear only when enabling Generic LPC via eSPI Decode 1.

Feature	Option	Description
Generic LPC via eSPI Decode 1	[Disabled], [Enabled]	Enable generic LPC via eSPI decode range.
Base Address	Value input	Base address of the generic decode range. Valid between 0100h – FFF0h. Must be 8-byte aligned. Please note that it also has to be length-aligned.
Length	Value input	Length of the generic decode range in hexadecimal notation. Valid between 0008h – 0100h. Must be multiple of 8h.

Figure 38: BIOS Advanced Menu – Miscellaneous – Watchdog

Aptio Setup – AMI			
Advanced			
Watchdog Configuration.			
Auto-reload	[Disabled]		
Global Lock	[Disabled]		
WDT Strobe	[Disabled]		
Stage 1 Mode	[Disabled]		
Assert WDT Signal ⁽¹⁾	[Disabled]		
Stage 1 Timeout ⁽²⁾	[1m]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit	
Stage 2 Mode ⁽³⁾	[Delay]		
Assert WDT Signal ⁽¹⁾	[Disabled]		
Stage 2 Timeout ⁽²⁾	[1m]		
Stage 3 Mode ⁽³⁾	[Delay]		
Assert WDT Signal ⁽¹⁾	[Disabled]		
Stage 3 Timeout ⁽²⁾	[1m]		
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⁽¹⁾ This item appears only when selecting Reset or Delay for Stage 1/2/3 Mode.

⁽²⁾ This item appears only when selecting Reset, Delay or WDT Signal only for Stage 1/2/3 Mode.

⁽³⁾ This item appears only when selecting Delay or WDT Signal only for Stange N-1 Mode.

Feature	Option	Description
Auto-reload	[Disabled], [Enabled]	Enable automatic reload of watchdog timers on timeout.
Global Lock	[Disabled], [Enabled]	If set to enabled, all Watchdog registers (except WD_KICK) become read only until the board is reset.
WDT Strobe	[Disabled], [Enabled]	Enable / disable WDT Strobe input.
Stage 1/2/3 Mode	[Disabled], [Reset], [Delay], [WDT Signal only]	Select Action for this Watchdog stage
Assert WDT Signal	[Disabled], [Enabled]	Enable / disable assertion of WDT signal to baseboard on stage timeout.
Stage 1/2/3 Timeout	[1m], [3m], [10m], [30m]	Select Timeout value for this Watchdog stage

Figure 39: BIOS Advanced Menu – H/W Monitor

Aptio Setup – AMI		
Advanced		
KSC based H/W Monitor		
Temperature sensors:		
#1: CPU Temp	: + 47.9 C	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
#2: PCH Temp	: + 45.0 C	
#3: SYSTEM Temp	: + 47.2 C	
Voltage sensors:		
#1: V_IN	: 12.2 V	
#2: 12V_S0	: 10.6 V	
#3: 5V_S0	: 5.1 V	
#4: 3V3_S0	: 3.4 V	
#5: 3V_BAT	: 2.9 V	
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Read only sub-screen

Figure 40: BIOS Advanced Menu – S5 RTC Wake Settings

Aptio Setup – AMI		
Advanced		
Wake system from S5	[Disabled]	
Wake up hour ⁽¹⁾	0	
Wake up minute ⁽¹⁾	0	→ ←: Select Screen
Wake up second ⁽¹⁾	0	↑ ↓: Select Item
Wake up minute increase ⁽²⁾	1	Enter: Select
		+/-: Change Opt.
		F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit
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⁽¹⁾ These items appear only when selecting Fixed Time for Wake system from S5.

⁽²⁾ This item appears only when selecting Dynamic Time for Wake system from S5.

Feature	Option	Description
Wake system from S5	[Disabled], [Fixed Time], [Dynamic Time]	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, system will wake on the current time + Increase minute(s).
Wake up hour	Value input	Select 0 – 23 For example, enter 3 for 3 am and 15 for 3 pm.
Wake up minute	Value input	Select 0 – 59 for Minute
Wake up second	Value input	Select 0 – 59 for Second
Wake up minute increase	Value input	1 – 5

Figure 41: BIOS Advanced Menu – Serial Port Console Redirection

Aptio Setup – AMI		
Advanced		
COM0		
Console Redirection	[Disabled]	
> Console Redirection Settings*		
COM1		
Console Redirection	[Disabled]	
> Console Redirection Settings*		
COM2		
Console Redirection	[Disabled]	
> Console Redirection Settings*		
COM3		
Console Redirection	[Disabled]	
> Console Redirection Settings*		
Legacy Console Redirection		
> Legacy Console Redirection Settings		
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS)		
Console Redirection EMS	[Disabled]	
> Console Redirection Settings*		
→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit		
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* These items activate only when enabling Console Redirection (EMS).

Feature	Option	Description
Console Redirection (EMS)	[Disabled], [Enabled]	Console Redirection Enable or Disable.

Figure 42: BIOS Advanced Menu – Serial Port Console Redirection – COM0/1/2/3 Console Redirection Settings

Aptio Setup – AMI		
Advanced		
COM0/1/2/3		
Console Redirection Settings		
Terminal Type	[ANSI]	
Bits per second	[115200]	
Data Bits	[8]	
→ ←: Select Screen ↑ ↓: Select Item		

Aptio Setup – AMI		
Advanced		
Parity	[None]	Enter: Select
Stop Bits	[1]	+/-: Change Opt.
Flow Control	[None]	F1: General Help
VT-UTF8 Combo Key Support	[Enabled]	F2: Previous Values
Recorder Mode	[Disabled]	F3: Optimized Defaults
Resolution 100x31	[Disabled]	F4: Save & Exit
Putty KeyPad	[VT100]	ESC: Exit
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Feature	Option	Description
Terminal Type	[VT100], [VT100Plus], [VT-UTF8], [ANSI]	Emulation: [ANSI]: Extended ASCII char set. [VT100]: ASCII char set. [VT100Plus]: Extends VT100 to support color, function keys, etc. [VT-UTF8]: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.
Bits per second	[9600], [19200], [38400], [57600], [115200]	Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
Data Bits	[7], [8]	Data Bits
Parity	[None], [Even], [Odd], [Mark], [Space]	A parity bit can be sent with the data bits to detect some transmission errors. [Even]: parity bit is 0 if the num of 1's in the data bits is even. [Odd]: parity bit is 0 if num of 1's in the data bits is odd. [Mark]: parity bit is always 1. [Space]: Parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.
Stop Bits	[1], [2]	Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.
Flow Control	[None], [Hardware RTS/CTS]	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start / stop signals.
VT-UTF8 Combo Key Support	[Disabled], [Enabled]	Enable VT-UTF8 Combination Key Support for ANSI / VT100 terminals
Recorder Mode	[Disabled], [Enabled]	With this mode enabled only text will be sent. This is to capture Terminal data.
Resolution 100x31	[Disabled], [Enabled]	Enables or disables extended terminal resolution

Feature	Option	Description
Putty KeyPad	[VT100], [LINUX], [XTERMR6], [SCO], [ESCN], [VT400]	Select FunctionKey and KeyPad on Putty.

Figure 43: BIOS Advanced Menu – Serial Port Console Redirection – Legacy Console Redirection Settings

Aptio Setup – AMI		
Advanced		
Legacy Console Redirection Settings		
Redirection COM Port	[COM2]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Resolution	[80x24]	
Redirect After POST	[Always Enable]	
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Feature	Option	Description
Redirection COM Port	[COM2], [COM3], [COM0], [COM1]	Select a COM port to display redirection of Legacy OS and Legacy OPRM Messages
Resolution	[80x24], [80x25]	On Legacy OS, the number of Rows and Columns supported redirection
Redirect After POST	[Always Enable], [BootLoader]	When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable.

Figure 44: BIOS Advanced Menu – Serial Port Console Redirection – Console Redirection EMS Settings

Aptio Setup – AMI		
Advanced		
Out-of-Band Mgmt Port	[COM2]	→ ←: Select Screen ↑ ↓: Select Item
Terminal Type EMS	[VT-UTF8]	
Bits per second EMS	[115200]	
Flow Control EMS	[None]	

Aptio Setup – AMI		
Advanced		
Data Bits EMS	8	Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Parity EMS	None	
Stop Bits EMS	1	
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Feature	Option	Description
Out-of-Band Mgmt Port	[COM2], [COM3], [COM0], [COM1]	Microsoft Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port.
Terminal Type EMS	[VT100], [VT100Plus], [VT-UTF8], [ANSI]	VT-UTF8 is the preferred terminal type for out-of-band management. The next best choice is VT100+ and then VT100. See above, in Console Redirection Settings page, for more Help with Terminal Type / Emulation.
Bits per second EMS	[9600], [19200], [57600], [115200]	Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
Flow Control EMS	[None], [Hardware RTS/CTS], [Software Xon/Xoff]	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start / stop signals.

Figure 45: BIOS Advanced Menu –AMI Graphic Output Protocol Policy

Aptio Setup – AMI	
Advanced	
Intel® Graphics Controller Intel® GOP Driver [21.0.1063]	
Output Select	[DP1 [ACTIVE]]
	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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Feature	Option	Description
Output Select	[DP1 [ACTIVE]]	Output Interface

Figure 46: BIOS Advanced Menu – SIO Configuration

Aptio Setup – AMI	
Advanced	
AMI SIO Driver Version: A5.19.00	
Super IO Chip Logical Devices(s) Configuration	→ ←: Select Screen
> [*Active*] Serial Port 0	↑ ↓: Select Item
> [*Active*] Serial Port 1	Enter: Select
> [*Active*] Serial Port 2	+/-: Change Opt.
> [*Active*] Serial Port 3	F1: General Help
WARNING: Logical Devices state on the left side of the control, reflects the current Logical Device state. Changes made during Setup Session will be shown after you restart the system.	F2: Previous Values
	F3: Optimized Defaults
	F4: Save & Exit
	ESC: Exit
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Figure 47: BIOS Advanced Menu – SIO Configuration – [*Active*] Serial Port 0

Aptio Setup – AMI	
Advanced	
Serial Port 0 Configuration	
Use This Device [Enabled]	→ ←: Select Screen
Logical Device Settings:*	↑ ↓: Select Item
Current: IO=3F8h; IRQ=4;*	Enter: Select
Possible:*	+/-: Change Opt.
[Use Automatic Settings]	F1: General Help
WARNING: Disabling SIO Logical Devices may have unwanted side effects. PROCEED WITH CAUTION.	F2: Previous Values
	F3: Optimized Defaults
	F4: Save & Exit
	ESC: Exit
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* These items appear only when enabling Use This Device.

Feature	Option	Description
Use This Device	[Disabled], [Enabled]	Enable or Disable this Logical Device.
Possible:	[Use Automatic Settings], [IO=3F8h; IRQ=4;], [IO=3F8h; IRQ=4;], [IO=2F8h; IRQ=3;]	Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

Figure 48: BIOS Advanced Menu – SIO Configuration – [*Active*] Serial Port 1

Aptio Setup – AMI	
Advanced	
Serial Port 1 Configuration	
Use This Device [Enabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Logical Device Settings:* Current: IO=2F8h; IRQ=3;*	
Possible:* [Use Automatic Settings]	
WARNING: Disabling SIO Logical Devices may have unwanted side effects. PROCEED WITH CAUTION.	
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* These items appear only when enabling Use This Device.

Feature	Option	Description
Use This Device	[Disabled], [Enabled]	Enable or Disable this Logical Device.
Possible:	[Use Automatic Settings], [IO=2F8h; IRQ=3;], [IO=2F8h; IRQ=3;], [IO=3F8h; IRQ=4;]	Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

Figure 49: BIOS Advanced Menu – SIO Configuration – [*Active*] Serial Port 2

Aptio Setup – AMI	
Advanced	
Serial Port 2 Configuration	
Use This Device [Enabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Logical Device Settings:* Current: IO=220h; IRQ=7;*	
Possible:* [Use Automatic Settings]	
WARNING: Disabling SIO Logical Devices may have unwanted side effects. PROCEED WITH CAUTION.	
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* These items appear only when enabling Use This Device.

Feature	Option	Description
Use This Device	[Disabled],	Enable or Disable this Logical Device.

Feature	Option	Description
	[Enabled]	
Possible:	[Use Automatic Settings], [IO=220h; IRQ=7; DMA;], [IO=220h; IRQ=5,6,7,10,11,12; DMA;]	Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

Figure 50: BIOS Advanced Menu – SIO Configuration – [*Active*] Serial Port 3

Aptio Setup – AMI	
Advanced	
Serial Port 3 Configuration	
Use This Device [Enabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Logical Device Settings:* Current: IO=230h; IRQ=10;*	
Possible:* [Use Automatic Settings]	
WARNING: Disabling SIO Logical Devices may have unwanted side effects. PROCEED WITH CAUTION.	
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* These items appear only when enabling Use This Device.

Feature	Option	Description
Use This Device	[Disabled], [Enabled]	Enable or Disable this Logical Device.
Possible:	[Use Automatic Settings], [IO=230h; IRQ=10; DMA;], [IO=230h; IRQ=5,6,7,10,11,12; DMA;]	Allows the user to change the device resource settings. New settings will be reflected on this setup page after system restarts.

Figure 51: BIOS Advanced Menu – USB Configuration

Aptio Setup – AMI		
Advanced		
USB Configuration		
USB Module Version	32	
USB Controllers: 2 XHCIs		
USB Devices: 1 Keyboard		
Legacy USB Support	[Enabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
XHCI Hand-off	[Enabled]	
USB Mass Storage Driver Support	[Enabled]	
USB hardware delays and time-outs:		
USB transfer time-out	[20 sec]	
Device reset time-out	[20 sec]	
Device power-up delay	[Auto]	
Device power-up delay in seconds*	5	
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* This item appears only when selecting Manual for Device power-up delay in seconds.

Feature	Option	Description
Legacy USB Support	[Enabled], [Disabled], [Auto]	Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications.
XHCI Hand-off	[Enabled], [Disabled]	This is a workaround for Oses without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
USB Mass Storage Driver Support	[Disabled], [Enabled]	Enable / Disable USB Mass Storage Driver Support.
USB transfer time-out	[1 sec], [5 sec], [10 sec], [20 sec]	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	[10 sec], [20 sec], [30 sec], [40 sec]	USB mass storage device Start Unit command time-out.
Device power-up delay	[Auto], [Manual]	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub

Feature	Option	Description
		descriptor.
Device power-up delay in seconds	Value input	Delay range is 1..40 seconds, in one second increments

Figure 52: BIOS Advanced Menu – Network Stack Configuration

Aptio Setup – AMI		
Advanced		
Network Stack	[Disabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
IPv4 PXE Support*	[Disabled]	
IPv4 HTTP Support*	[Disabled]	
IPv6 PXE Support*	[Disabled]	
IPv6 HTTP Support*	[Disabled]	
PXE boot wait time*	0	
Media detect count*	1	
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* These items appear only when enabling Network Stack.

Feature	Option	Description
Network Stack	[Disabled], [Enabled]	Enable / Disable UEFI Network Stack
IPv4 PXE Support	[Disabled], [Enabled]	Enable / Disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.
IPv4 HTTP Support	[Disabled], [Enabled]	Enable / Disable IPv4 HTTP boot support. If disabled, IPv4 HTTP boot support will not be available.
IPv6 PXE Support	[Disabled], [Enabled]	Enable / Disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.
IPv6 HTTP Support	[Disabled], [Enabled]	Enable / Disable IPv6 HTTP boot support. If disabled, IPv6 HTTP boot support will not be available.
PXE boot wait time	Value input	Wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the value.
Media detect count	Value input	Number of times the presence of media will be checked. Use either +/- or numeric keys to set the value.

Figure 53: BIOS Advanced Menu – NVMe Configuration

Aptio Setup - AMI					
Main	Advanced	Power	Boot	Security	Save & Exit
NVMe Configuration					
No NVME Device Found				→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Reset ESC: Exit	
Version 2.22.1282. Copyright (C) 2023, AMI					

Figure 54: BIOS Advanced Menu – F81435 Configurations

Aptio Setup – AMI		
Advanced		
F81435 Configurations		
COM1 Mode Selection	[RS-232]	
COM1 Transceiver	[Normal mode]	
COM1 Internal Terminator Switch Control	[Terminator switch is disabled]	
COM1 External Terminator Switch Control	[Terminator switch is disabled]	
COM2 Mode Selection	[RS-232]	
COM2 Transceiver	[Normal mode]	
COM2 Internal Terminator Switch Control	[Terminator switch is disabled]	
COM2 External Terminator Switch Control	[Terminator switch is disabled]	
COM3 Mode Selection	[RS-232]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
COM3 Transceiver	[Normal mode]	
COM3 Internal Terminator Switch Control	[Terminator switch is disabled]	
COM3 External Terminator Switch Control	[Terminator switch is disabled]	
COM4 Mode Selection	[RS-232]	
COM4 Transceiver	[Normal mode]	
COM4 Internal Terminator Switch Control	[Terminator switch is disabled]	
COM4 External Terminator Switch Control	[Terminator switch is disabled]	

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Feature	Option	Description
COM1/2/3/4 Mode Selection	[RS-422 Signal Master], [RS-232], [RS-485 with Auto Flow Control], [RS-422 Multi Master]	Mode selection for COM1/2/3/4
COM1/2/3/4 Transceiver	[Shutdown mode], [Normal mode]	Shutdown the Transceiver of COM1/2/3/4
COM1/2/3/4 Internal Terminator Switch Control	[Terminator switch is disabled], [Terminator switch is enabled]	Internal Terminator switch control for RS-422/RS-485 of COM1/2/3/4
COM1/2/3/4 External Terminator Switch Control	[Terminator switch is disabled], [Terminator switch is enabled]	External Terminator switch control for RS-422/RS-485 of COM1/2/3/4

Figure 55: BIOS Advanced Menu – Intel® Ethernet Controller I226-V – C0:EA:C3:D1:D0:CA/CB

Aptio Setup – AMI		
Advanced		
UEFI Driver	Intel® 2.5G Ethernet Controller 0.10.06	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Device Name	Intel® Ethernet Controller I226-V	
Link Status	[Disconnected]	
MAC Address	C0:EA:C3:D1:D0:CA/CB	
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Read only sub-screen

12.2.3. Chipset Setup Menu

The Chipset setup menu provides functions and a sub-screen for chipset configurations. The following sub-screen functions are included in the menu:

- ▶ System Agent (SA) Configuration
- ▶ PCH-IO Configuration

Figure 56: BIOS Chipset Setup Menu

Aptio Setup – AMI					
Main	Advanced	Chipset	Security	Boot	Save & Exit
> System Agent (SA) Configuration > PCH-IO Configuration					
				→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit	
Version 2.22.1293 Copyright (C) 2025 AMI					

Figure 57: BIOS Chipset Setup Menu –System Agent (SA) Configuration

Aptio Setup – AMI		
Chipset		
System Agent (SA) Configuration		
VT-d	Supported	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
> Graphics Configuration		
VT-d	[Enabled]	
Above 4GB MMIO BIOS assignment	[Enabled]	
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Feature	Option	Description
VT-d	[Enabled], [Disabled]	VT-d capability
Above 4GB MMIO BIOS assignment	[Enabled], [Disabled]	Enable / Disable above 4GB MemoryMappedIO BIOS assignment. This is enabled automatically when Aperture Size is set to 2048MB.

Figure 58: BIOS Chipset Setup Menu –System Agent (SA) Configuration –Graphics Configuration

Aptio Setup – AMI		
Chipset		
Graphics Configuration		
Graphics Turbo IMON Current	31	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help
Skip Scanning of External Gfx Card	[Disabled]	
> External Gfx Card Primary Display Configuration		
GTT Size	[8MB]	
PSMI SUPPORT	[Disabled]	
PSMI Region Size ⁽¹⁾	[32MB]	
Intel Graphics Pei Display Peim	[Disabled]	
VDD Enable	[Enabled]	
Configure GT for use	[Enabled]	
RC1p Support ⁽²⁾	[Disabled]	
PAVP Enable	[Enabled]	
Cdynmax Clamping Enable	[Disabled]	
Cd Clock Frequency	[Max CdClock freq based on Reference Clk]	

Aptio Setup – AMI		
Chipset		
Enable Display Audio Link in Pre-OS	[Disabled]	F2: Previous Values
IUER Button Enable	[Disabled]	F3: Optimized Defaults
> LCD Control		F4: Save & Exit
> Intel® Ultrabook Event Support		ESC: Exit
Version 2.22.1293 Copyright (C) 2025 AMI		

⁽¹⁾ This item appears only when enabling PSMI SUPPORT.

⁽²⁾ This item appears only when enabling Configure GT for use.

Feature	Option	Description
Graphics Turbo IMON Current	Value input	Graphics turbo IMON current values supported (14 - 31)
Skip Scanning of External Gfx Card	[Disabled], [Enabled]	If Enable, it will not scan for External Gfx Card on PEG and PCH PCIE Ports
GTT Size	[2MB], [4MB], [8MB]	Select the GTT Size
PSMI SUPPORT	[Disabled], [Enabled]	PSMI Enable / Disable
PSMI Region Size	[32MB], [288MB], [544MB], [800MB], [1024MB]	Select the PSMI Region Size: Range from 32MB to 1024MB
Intel Graphics Pei Display Peim	[Enabled], [Disabled]	Enable / Disable Pei (Early) Display
VDD Enable	[Disabled], [Enabled]	Enable / Disable forcing of VDD in the BIOS
Configure GT for use	[Enabled], [Disabled]	Enable / Disable GT configuration in BIOS
RC1p Support	[Enabled], [Disabled]	Enable / Disable RC1p support. If RC1p is enabled, send a RC1p frequency request to PMA based other conditions being met
PAVP Enable	[Enabled], [Disabled]	Enable / Disable PAVP
Cdynmax Clamping Enable	[Enabled], [Disabled]	Enable / Disable Cdynmax Clamping
Cd Clock Frequency	[192 Mhz], [307.2 Mhz], [556.8 Mhz], [652.8 Mhz], [Max CdClock freq based on Reference Clk]	Select the highest Cd Clock frequency supported by the platform
Enable Display Audio Link in Pre-OS	[Disabled], [Enabled]	[Enabled]: Display Audio Link will be enabled in Pre-OS. [Disabled]: Display Audio Link will be disabled in Pre-OS.

Feature	Option	Description
IUER Button Enable	[Disabled], [Enabled]	Enable / Disable IUER Button Functionality

Figure 59: BIOS Chipset Setup Menu – System Agent (SA) Configuration – Graphics Configuration – External Gfx Card Primary Display Configuration

Aptio Setup – AMI	
Chipset	
External Gfx Card Primary Display Configuration	
	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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Figure 60: BIOS Chipset Setup Menu – System Agent (SA) Configuration – Graphics Configuration – LCD Control

Aptio Setup – AMI	
Chipset	
LCD Control	
LCD Panel Type	[VBIOS Default]
Panel Scaling	[Auto]
Backlight Control	[PWM Normal]
Active LFP	[eDP Port-A]
Panel Color Depth	[18 Bit]
Backlight Brightness	255
	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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Feature	Option	Description
LCD Panel Type	[VBIOS Default], [640x480 LVDS], [800x600 LVDS], [1024x768 LVDS], [1280x1024 LVDS],	Select LCD panel used by Internal Graphics Device by selecting the appropriate setup item.

Feature	Option	Description
	[1400x1050 LVDS1], [1400x1050 LVDS2], [1600x1200 LVDS], [1280x768 LVDS], [1680x1050 LVDS], [1920x1200 LVDS], [1600x900 LVDS], [1280x800 LVDS], [1280x600 LVDS], [2048x1536 LVDS], [1366x768 LVDS]	
Panel Scaling	[Auto], [Off], [Force Scaling]	Select the LCD panel scaling option used by the Internal Graphics Device.
Backlight Control	[PWM Inverted], [PWM Normal]	Back Light Control Setting
Active LFP	[No eDP], [eDP Port-A]	Select the Active LFP Configuration. [No LVDS]: VBIOS does not enable LVDS. [Int-LVDS]: VBIOS enables LVDS driver by Integrated encoder. [SDVO LVDS]: VBIOS enables LVDS driver by SDVO encoder. [eDP Port-A]: LFP Driven by Int-DisplayPort encoder from Port-A. [eDP Port-D]: LFP Driven by Int-DisplayPort encoder from Port-D (through PCH).
Panel Color Depth	[18 Bit], [24 Bit]	Select the LFP Panel Color Depth
Backlight Brightness	Value input	Set VBIOS Brightness. Range: 0 – 255.

Figure 61: BIOS Chipset Setup Menu – System Agent (SA) Configuration – Graphics Configuration – Intel® Ultrabook Event Support

Aptio Setup – AMI		
Chipset		
Intel® Ultrabook Event Support		
IUER Slate Enable	[Disabled]	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Slate Mode boot value ⁽¹⁾	[Laptop Mode]	
Slate Mode on S3 and S4 resume ⁽¹⁾	[No change]	
IUER Dock Enable	[Disabled]	
Dock Mode boot value ⁽²⁾	[Undocked]	
Dock Mode upon S3 and S4 resume ⁽²⁾	[No change]	
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⁽¹⁾ These items appear only when enabling IUER Slate Enable.

⁽²⁾ These items appear only when enabling IUER Dock Enable.

Feature	Option	Description
IUER Slate Enable	[Disabled], [Enabled]	Enable / Disable IUER Slate Functionality
Slate Mode boot value	[Slate Mode], [Laptop Mode]	Choose Slate or Laptop as boot mode.
Slate Mode on S3 and S4 resume	[No change], [Toggle]	Keep it the same as Sx entry or toggle it.
IUER Dock Enable	[Disabled], [Enabled]	Enable / Disable IUER Dock Functionality
Dock Mode boot value	[Undocked], [Docked]	Choose Docked or Undocked as boot mode.
Dock Mode upon S3 and S4 resume	[No change], [Toggle]	Keep it the same as Sx entry or toggle it.

Figure 62: BIOS Chipset Setup Menu – PCH-IO Configuration

Aptio Setup – AMI		
Chipset		
PCH-IO Configuration		
> PCI Express Configuration > SATA Configuration > USB Configuration > TSN GBE Configuration#		
PCH LAN Controller	No GbE Region	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Port 80h Redirection	[LPC Bus]	
Enhance Port 80h LPC Decoding*	[Enabled]	
Compatible Revision ID	[Disabled]	
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* This item activates only when selecting LPC Bus for Port 80h Redirection.

This sub-menu is not accessible.

Feature	Option	Description
Port 80h Redirection	[LPC Bus], [PCIe Bus]	Control where the Port 80h cycles are sent.
Enhance Port 80h LPC Decoding	[Disabled], [Enabled]	Support the word / dword decoding of port 80h behind LPC
Compatible Revision ID	[Disabled]	Read only item

Figure 63: BIOS Chipset Setup Menu – PCH-IO Configuration – PCI Express Configuration

Aptio Setup – AMI		
Chipset		
PCI Express Configuration		
DMI Link ASPM Control	[Auto]	
Port8xh Decode	[Disabled]	
Port8xh Decode Port#*	0	
PCIe function swap	[Enabled]	
PCH PCIe Clock Gating	[Disabled]	
PCH PCIe Power Gating	[Disabled]	
> PCIe EQ settings		
PCI Express Root Port 1	Lane configured as USB / SATA / UFS	
PCI Express Root Port 2	Lane configured as USB / SATA / UFS	
> PCI Express Root Port 3		

Aptio Setup – AMI		
Chipset		
> PCI Express Root Port 4		
PCI Express Root Port 5	Not present in this SKU	
PCI Express Root Port 6	Not present in this SKU	→ ←: Select Screen
> PCI Express Root Port 7		↑ ↓: Select Item
PCI Express Root Port 8	Not present in this SKU	Enter: Select
> PCI Express Root Port 9		+/-: Change Opt.
> PCI Express Root Port 10		F1: General Help
PCI Express Root Port 11	Lane configured as USB / SATA / UFS	F2: Previous Values
PCI Express Root Port 12	Lane configured as USB / SATA / UFS	F3: Optimized Defaults
		F4: Save & Exit
> PCIe clocks		ESC: Exit
Version 2.22.1293 Copyright (C) 2025 AMI		

* This item appears only when enabling Port8xh Decode.

Feature	Option	Description
DMI Link ASPM Control	[Disabled], [L0s], [L1], [L0sL1], [Auto]	The control of Active State Power Management of the DMI Link.
Port8xh Decode	[Disabled], [Enabled]	PCI Express Port8xh Decode Enable / Disable.
Port8xh Decode Port#	Value input	Select PCI Express Port8xh Decode Root Port. User to ensure port availability
PCIe function swap	[Disabled], [Enabled]	When Disabled, prevents PCIe rootport function swap. If any function other than 0th is enabled, 0th will become visible.
PCH PCIe Clock Gating	[Disabled], [Enabled]	PCH PCI Express Clock Gating Enable / Disable for all port
PCH PCIe Power Gating	[Disabled], [Enabled]	PCH PCI Express Power Gating Enable / Disable for all port

Figure 64: BIOS Chipset Setup Menu – PCH-IO Configuration – PCI Express Configuration – PCIe EQ settings

Aptio Setup – AMI		
Chipset		
PCIe EQ override	[Disabled]	
PCIe EQ method*	[PCIe hardware EQ]	
PCIe EQ mode*	[Use presets during EQ]	
EQ PH1 downstream port transmitter present*	0	
EQ PH1 upstream port transmitter present*	0	
Enable EQ phase 2 local transmitter override*	[Disabled]	
Number of presents or coefficients used during phase 3*	0	

Aptio Setup – AMI		
Chipset		
Preset 0 ^{*(1)}	0	
Preset 1 ^{*(1)}	0	
Preset 2 ^{*(1)}	0	
Preset 3 ^{*(1)}	0	
Preset 4 ^{*(1)}	0	
Preset 5 ^{*(1)}	0	
Preset 6 ^{*(1)}	0	
Preset 7 ^{*(1)}	0	
Preset 8 ^{*(1)}	0	
Preset 9 ^{*(1)}	0	
Preset 10 ^{*(1)}	0	
Pre-cursor coefficient 0 ^{*(2)}	0	
Post-cursor coefficient 0 ^{*(2)}	0	
Pre-cursor coefficient 1 ^{*(2)}	0	
Post-cursor coefficient 1 ^{*(2)}	0	
Pre-cursor coefficient 2 ^{*(2)}	0	
Post-cursor coefficient 2 ^{*(2)}	0	
Pre-cursor coefficient 3 ^{*(2)}	0	
Post-cursor coefficient 3 ^{*(2)}	0	
Pre-cursor coefficient 4 ^{*(2)}	0	
Post-cursor coefficient 4 ^{*(2)}	0	
Pre-cursor coefficient 5 ^{*(2)}	0	
Post-cursor coefficient 5 ^{*(2)}	0	→ ←: Select Screen
Pre-cursor coefficient 6 ^{*(2)}	0	↑ ↓: Select Item
Post-cursor coefficient 6 ^{*(2)}	0	Enter: Select
Pre-cursor coefficient 7 ^{*(2)}	0	+/-: Change Opt.
Post-cursor coefficient 7 ^{*(2)}	0	F1: General Help
Pre-cursor coefficient 8 ^{*(2)}	0	F2: Previous Values
Post-cursor coefficient 8 ^{*(2)}	0	F3: Optimized Defaults
Pre-cursor coefficient 9 ^{*(2)}	0	F4: Save & Exit
Post-cursor coefficient 9 ^{*(2)}	0	ESC: Exit
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* These items appear only when enabling PCIe EQ override.

⁽¹⁾ These items appear only when selecting Use presets during EQ for PCIe EQ mode.

⁽²⁾ These items appear only when selecting Use coefficients during EQ for PCIe EQ mode.

Feature	Option	Description
PCIe EQ override	[Disabled], [Enabled]	Choose your own PCIe EQ settings, only for users who have a thorough understanding of equalization process
PCIe EQ method	[PCIe hardware EQ], [PCIe fixed EQ]	Choose PCIe EQ method
PCIe EQ mode	[Use presets during	Choose EQ mode.

Feature	Option	Description
	EQ], [Use coefficients during EQ]	Preset mode – root port will use presets during EQ process, Coefficient mode – root port will use coefficients during EQ process
EQ PH1 downstream port transmitter preset	Value input	Choose the value of the preset that will be used during phase 1 of the equalization
EQ PH1 upstream port transmitter preset	Value input	Choose the value of the preset that will be used during phase 1 of the equalization
Enable EQ phase 2 local transmitter override	[Disabled], [Enabled]	EQ Phase 2 local transmitter override can be used to debug issues with PCI devices equalization.
Number of presets or coefficients used during phase 3	Value input	Select how many presets or coefficients will be used during phase 3 of EQ. Please not that you have to set all of the list entries to valid values. The interpretation of this field depends on PCIe EQ mode
Preset 0..10	Value input	Choose the target preset value
Pre-cursor coefficient 0..9	Value input	Choose the target pre-cursor coefficient value
Post-cursor coefficient 0..9	Value input	Choose the target post-cursor coefficient value

Figure 65: BIOS Chipset Setup Menu – PCH-I/O Configuration – PCI Express Configuration – PCI Express Root Port 3 / 4 / 7 / 9 / 10

Aptio Setup – AMI	
Chipset	
PCI Express Root Port 3 / 4 / 7 / 9 / 10	[Enabled]
Connection Type*	[Slot]
ASPM*	[Auto]
L1 Substates*	[L1.1 & L1.2]
L1 Low*	[Enabled]
ACS*	[Enabled]
PTM*	[Enabled]
DPC*	[Disabled]
EDPC*	[Enabled]
URR*	[Disabled]
FER*	[Disabled]
NFER*	[Disabled]
CER*	[Disabled]
SEFE*	[Disabled]
SENF*	[Disabled]
SECE*	[Disabled]
PME SCI*	[Enabled]
Hot Plug*	[Disabled]
Advanced Error Reporting*	[Enabled]
PCIe Speed*	[Auto]

Aptio Setup – AMI		
Chipset		
Transmitter Half Swing*	[Disabled]	
Detect Timeout*	0	
Extra Bus Reserved*	0	
Reserved Memory*	10	
Reserved I/O*	4	
PCH PCIe LTR Configuration*		
LTR*	[Enabled]	
Snoop Latency Override**	[Auto]	→ ←: Select Screen
Snoop Latency Value** ⁽¹⁾	60	↑ ↓: Select Item
Snoop Latency Multiplier** ⁽¹⁾	[1024 ns]	Enter: Select
Non Snoop Latency Override**	[Auto]	+/-: Change Opt.
Non Snoop Latency Value** ⁽²⁾	60	F1: General Help
Non Snoop Latency Multiplier** ⁽²⁾	[1024 ns]	F2: Previous Values
LTR Lock*	[Disabled]	F3: Optimized Defaults
Peer Memory Write Enable*	[Disabled]	F4: Save & Exit
		ESC: Exit
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* These items appear only when enabling PCI Express Root Port 3 / 4 / 7 / 9 / 10.

These items appear only when enabling LTR.

⁽¹⁾ These items appear only when selecting Manual for Snoop Latency Override.

⁽²⁾ These items appear only when selecting Manual for Mon Snoop Latency Override.

Feature	Option	Description
PCI Express Root Port 3 / 4 / 7 / 9 / 10	[Disabled], [Enabled]	Control the PCI Express Root Port.
Connection Type	[Bulit-in], [Slot]	[Built-in]: a built-in device is connected to this rootport. SlotImplemented bit will be clear. [Slot]: this rootport connects to user-accessible slot. SlotImplemented bit will be set.
ASPM	[Disabled], [L1], [Auto]	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM
L1 Substates	[Disabled], [L1.1], [L1.1 & L1.2]	PCI Express L1 Substates settings.
L1 Low	[Disabale], [Enabled]	PCI Express L1 Low Substate Enable / Disable.
ACS	[Disabled], [Enabled]	Enable / Disable Access Control Services Extended Capability
PTM	[Disabled], [Enabled]	Enable / Disable Precision Time Measurement

Feature	Option	Description
DPC	[Disabled], [Enabled]	Enable / Disable Downstream Port Containment
EDPC	[Disabled], [Enabled]	Enable / Disable Rootport extensions for Downstream Port Containment
URR	[Disabled], [Enabled]	PCI Express Unsupported Request Reporting Enable / Disable.
FER	[Disabled], [Enabled]	PCI Express Device Fatal Error Reporting Enable / Disable.
NFER	[Disabled], [Enabled]	PCI Express Device Non-Fatal Error Reporting Enable / Disable.
CER	[Disabled], [Enabled]	PCI Express Device Correctable Error Reporting Enable / Disable.
SEFE	[Disabled], [Enabled]	Root PCI Express System Error on Fatal Error Enable / Disable.
SENF	[Disabled], [Enabled]	Root PCI Express System Error on Non-Fatal Error Enable / Disable.
SECE	[Disabled], [Enabled]	Root PCI Express System Error on Correctable Error Enable / Disable.
PME SCI	[Disabled], [Enabled]	PCI Express PME SCI Enable / Disable.
Hot Plug	[Disabled], [Enabled]	PCI Express Hot Plug Enable / Disable.
Advanced Error Reporting	[Disabled], [Enabled]	Advanced Error Reporting Enable / Disable.
PCIe Speed	[Auto], [Gen1], [Gen2], [Gen3]	Configure PCIe Speed
Transmitter Half Swing	[Disabled], [Enabled]	Transmitter Half Swing Enable / Disable.
Detect Timeout	Value input	The number of milliseconds reference code will wait for link to exit Detect state for enabled ports before assuming there is no device and potentially disabling the port.
Extra Bus Reserved	Value input	Extra Bus Reserved (0-7) for bridges behind this Root Bridge.
Reserved Memory	Value input	Reserved Memory for this Root Bridge (1-20) MB
Reserved I/O	Value input	Reserved I/O (4K/8K/12K/16K/20K) Range for this Root Bridge.
LTR	[Disabled], [Enabled]	PCH PCIE Latency Reporting Enable / Disable
Snoop Latency Override	[Disabled], [Manual], [Auto]	Snoop Latency Override for PCH PCIE. [Disabled]: Disable override. [Manual]: Manually enter override values. [Auto] (default): Maintain default BIOS flow.
Snoop Latency Value	Value input	LTR Snoop Latency value of PCH PCIE
Snoop Latency Multiplier	[1 ns], [32 ns], [1024 ns],	LTR Snoop Latency Multiplier of PCH PCIE

Feature	Option	Description
	[32768 ns], [1048576 ns], [33554432 ns]	
Non Snoop Latency Override	[Disabled], [Manual], [Auto]	Non Snoop Latency Override for PCH PCIE. [Disabled]: Disable override. [Manual]: Manually enter override values. [Auto] (default): Maintain default BIOS flow.
Non Snoop Latency Value	Value input	LTR Non Snoop Latency value of PCH PCIE
Non Snoop Latency Multiplier	[1 ns], [32 ns], [1024 ns], [32768 ns], [1048576 ns], [33554432 ns]	LTR Non Snoop Latency Multiplier of PCH PCIE
LTR Lock	[Disabled], [Enabled]	PCIE LTR Configuration Lock
Peer Memory Write Enable	[Disabled], [Enabled]	Peer Memory Write Enable / Disable

Figure 66: BIOS Chipset Setup Menu –PCH-IO Configuration –PCI Express Configuration –PCIE clocks

Aptio Setup – AMI		
Chipset		
Clock0 assignment	[Platform-POR]	
ClkReq for Clock0	[Platform-POR]	
Clock1 assignment	[Platform-POR]	
ClkReq for Clock1	[Platform-POR]	
Clock2 assignment	[Platform-POR]	
ClkReq for Clock2	[Platform-POR]	
Clock3 assignment	[Platform-POR]	
ClkReq for Clock3	[Platform-POR]	
Clock4 assignment	[Platform-POR]	
ClkReq for Clock4	[Platform-POR]	
Clock5 assignment	[Platform-POR]	
ClkReq for Clock5	[Platform-POR]	
Clock6 assignment	[Platform-POR]	
ClkReq for Clock6	[Platform-POR]	
Clock7 assignment	[Platform-POR]	
ClkReq for Clock7	[Platform-POR]	
Clock8 assignment	[Platform-POR]	
ClkReq for Clock8	[Platform-POR]	
Clock9 assignment	[Platform-POR]	
ClkReq for Clock9	[Platform-POR]	
		→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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Feature	Option	Description
Clock0..9 assignment	[Platform-POR], [Enabled], [Disabled]	[Platform-POR]: clock is assigned to PCIe port or LAN according to board layout. [Enabled]: keep clock enabled even if unused. [Disabled]: Disable clock.
ClkReq for Clock0..9	[Platform-POR], [Disabled]	[Platform-POR]: CLKREQ signal is assigned to CLKSRC according to board layout. [Disabled]: CLKREQ will not be used.

Figure 67: BIOS Chipset Setup Menu – PCH-IO Configuration – SATA Configuration

Aptio Setup – AMI	
Chipset	
SATA Configuration	
SATA Controller(s)	[Enabled]
SATA Mode Selection*	[AHCI]
SATA Test Mode*	[Disabled]
Aggressive LPM Support ^{*(1)}	[Enabled]
Serial ATA Port 0*	TS256GMTS960T (256.0GB)
Software Preserve*	SUPPORTED
Port 0*	[Enabled]
Hot Plug*	[Disabled]
Configured as eSATA ^{*(3)}	Hot Plug supported
External*	[Disabled]
Mechanical Presence Switch ^{*(2)}	[Disabled]
Spin Up Device*	[Disabled]
SATA Device Type*	[Hard Disk Drive]
Topology*	[Unknown]
SATA Port 0 DevSlp*	[Disabled]
DITO Configuration*	[Disabled]
DITO Value ^{*(4)}	625
DM Value ^{*(4)}	15
Serial ATA Port 1*	TS256GSSD460K (256.0GB)
Software Preserve*	SUPPORTED
Port 1*	[Enabled]
Hot Plug*	[Disabled]
Configured as eSATA ^{*(3)}	Hot Plug supported
External*	[Disabled]
Mechanical Presence Switch ^{*(2)}	[Disabled]
Spin Up Device*	[Disabled]
SATA Device Type*	[Hard Disk Drive]
Topology*	[Unknown]

Aptio Setup – AMI		
Chipset		
SATA Port 1 DevSlp*	[Disabled]	
DITO Configuration*	[Disabled]	
DITO Value ^{*(4)}	625	
DM Value ^{*(4)}	15	
Serial ATA Port 2*	Empty	
Software Preserve*	Unknown	
Port 2*	[Enabled]	
Hot Plug*	[Disabled]	
Configured as eSATA ^{*(3)}	Hot Plug supported	
External*	[Disabled]	→ ←: Select Screen
Mechanical Presence Switch ^{*(2)}	[Disabled]	↑ ↓: Select Item
Spin Up Device*	[Disabled]	Enter: Select
SATA Device Type*	[Hard Disk Drive]	+/-: Change Opt.
Topology*	[Unknown]	F1: General Help
SATA Port 2 DevSlp*	[Disabled]	F2: Previous Values
DITO Configuration*	[Disabled]	F3: Optimized Defaults
DITO Value ^{*(4)}	625	F4: Save & Exit
DM Value ^{*(4)}	15	ESC: Exit
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* These items appear only when enabling SATA Controller(s).

(1) This item appears only when disabling SATA Test Mode.

(2) This item appears only when enabling Hot Plug.

(3) This item appears only when disabling External.

(4) These items activate only when enabling DITO Configuration.

Feature	Option	Description
SATA Controller(s)	[Enabled], [Disabled]	Enable / Disable SATA Device.
SATA Mode Selection	[AHCI]	Determines how SATA controller(s) operate.
SATA Test Mode	[Enabled], [Disabled]	Test Mode Enable / Disable (Loop Back).
Aggressive LPM Support	[Disabled], [Enabled]	Enable PCH to aggressively enter link power state.
Port 0..2	[Disabled], [Enabled]	Enable or Disable SATA Port
Hot Plug	[Disabled], [Enabled]	Designates this port as Hot Pluggable.
External	[Disabled], [Enabled]	Marks this port as external.
Mechanical Presence Switch	[Disabled], [Enabled]	Controls reporting if this port has an Mechanical Presence Switch. Note: Requires hardware support.

Feature	Option	Description
Spin Up Device	[Disabled], [Enabled]	If enabled for any of ports Staggered Spin Up will be performed and only the drives which have this option enabled will spin up at boot. Otherwise all drives spin up at boot.
SATA Device Type	[Hard Disk Drive], [Solid State Drive]	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive
Topology	[Unknown], [ISATA], [Direct Connect], [Flex], [M2]	Identify the SATA Topology if it is Default or ISATA or Flex or DirectConnect or M2
SATA Port 0..2 DevSlp	[Disabled], [Enabled]	Enable / Disable SATA Port 0..2 DevSlp. For DevSlp to work, both hard drive and SATA port need to support DevSlp function, otherwise an unexpected behavior might happen. Please check board design before enabling it.
DITO Configuration	[Disabled], [Enabled]	Enable / Disable DITO Configuration
DITO Value	Value input	DITO Value
DM Value	Value input	DM Value

Figure 68: BIOS Chipset Setup Menu – PCH-IO Configuration – USB Configuration

Aptio Setup – AMI	
Chipset	
USB Configuration	
xHCI Support	[Disabled]
USB2 PHY Sus Well Power Gating	[Enabled]
USB PDO Programming	[Enabled]
USB Overcurrent	[Enabled]
USB Overcurrent Lock	[Enabled]
USB Audio Offload	[Enabled]
Enable HSII on xHCI	[Enabled]
USB3.1 Portx Speed Selection	0
USB Port Disable Override	[Disabled]
USB SW Device Mode Port #0*	[Disabled]
USB SW Device Mode Port #1*	[Disabled]
USB SW Device Mode Port #2*	[Disabled]
USB SW Device Mode Port #3*	[Disabled]
USB SW Device Mode Port #4*	[Disabled]
USB SW Device Mode Port #5*	[Disabled]
USB SW Device Mode Port #6*	[Disabled]
USB SW Device Mode Port #7*	[Disabled]

Aptio Setup – AMI		
Chipset		
USB SW Device Mode Port #8*	[Disabled]	
USB SW Device Mode Port #9*	[Disabled]	
USB SS Physical Connector #0*	[Enabled]	
USB SS Physical Connector #1*	[Enabled]	
USB SS Physical Connector #2*	[Enabled]	
USB SS Physical Connector #3*	[Enabled]	
USB HS Physical Connector #0*	[Enabled]	
USB HS Physical Connector #1*	[Enabled]	→ ←: Select Screen
USB HS Physical Connector #2*	[Enabled]	↑ ↓: Select Item
USB HS Physical Connector #3*	[Enabled]	Enter: Select
USB HS Physical Connector #4*	[Enabled]	+/-: Change Opt.
USB HS Physical Connector #5*	[Enabled]	F1: General Help
USB HS Physical Connector #6*	[Enabled]	F2: Previous Values
USB HS Physical Connector #7*	[Enabled]	F3: Optimized Defaults
USB HS Physical Connector #8*	[Disabled]	F4: Save & Exit
USB HS Physical Connector #9*	[Enabled]	ESC: Exit
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* These items appear only when selecting Select Per-Pin for USB Port Disable Override.

Feature	Option	Description
xDCI	[Disabled], [Enabled]	Enable / Disable xDCI (USB OTG Device).
USB2 PHY Sus Well Power Gating	[Disabled], [Enabled]	Select 'Enabled' to enable SUS Well PG for USB2 PHY. This option has no effect on PCH-H.
USB PDO Programming	[Disabled], [Enabled]	Select 'Enabled' if Port Disable Override functionality is used.
USB Overcurrent	[Disabled], [Enabled]	Select 'Disabled' for pin-based debug. If pin-based debug is enabled but USB overcurrent is not disabled, USB DbC does not work.
USB Overcurrent Lock	[Disabled], [Enabled]	Select 'Disabled' if Overcurrent functionality is used. Enabling this will make xHCI controller consume the Overcurrent mapping data
USB Audio Offload	[Disabled], [Enabled]	Enable / Disable USB Audio Offload functionality
Enable HSII on xHCI	[Disabled], [Enabled]	Enable / Disable HSII feature. It may lead to increased power consumption.
USB3.1 Portx Speed Selection	Value input	Port Selection value in decimal for Gen1; Default – Gen2; Bit 0 corresponds to Port 0 and so on.
USB Port Disable Override	[Disabled], [Select Per-Pin]	Selectively Enable / Disable the corresponding USB port from reporting a Device Connection to the controller.
USB SW Device Mode Port #0..9	[Disabled], [Enabled]	Enable Connector Event for device subscription.
USB SS Physical Connector #0..3	[Disabled], [Enabled]	Enable / Disable this USB Physical Connector (physical port). Once disabled, any USB devices plug into the connector will not

Feature	Option	Description
		be detected by BIOS or OS.
USB HS Physical Connector #0..9	[Disabled], [Enabled]	Enable / Disable this USB Physical Connector (physical port). Once disabled, any USB devices plug into the connector will not be detected by BIOS or OS.

12.2.4. Security Setup Menu

The Security setup menu provides information about the passwords and functions for specifying the security settings. The passwords are case-sensitive. The KBox N-120-ADN provides no factory-set passwords.

NOTICE	<p>If there is already a password installed, the system asks for this first. To clear a password, simply enter nothing and acknowledge by pressing <ENTER>. To set a password, enter it twice and acknowledge by pressing <ENTER>.</p>
---------------	--

Figure 69: BIOS Security Setup Menu

Aptio Setup – AMI					
Main	Advanced	Chipset	Security	Boot	Save & Exit
Password Description					
If ONLY the Administrator's password is set, then this only limits access to Setup and is only asked for when entering Setup					
If ONLY the User's password is set, then this is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights					
The password length must be in the following range:					
Minimum Length		3			
Maximum length		20			
Administrator Password					
User Password					
> Secure Boot					
			→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit		
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Feature	Description
Administrator Password	Set administrator password
User Password	Set user password



If only the administrator's password is set, then only access to setup is limited. The password is only entered when entering setup.

If only the user's password is set, then the password is a power on password and must be entered to boot or enter setup. Within the setup menu the user has administrator rights.

Password length requirements are maximum 20 characters and minimum 3 characters.

Figure 70: BIOS Security Setup Menu – Secure Boot

Aptio Setup – AMI		
Security		
System Mode	Setup	
Secure Boot	[Enabled] Not Active	→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Secure Boot Mode	[Custom]	
> Restore Factory Keys*		
> Reset To Setup Mode*		
> Key Management*		
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*These items are selectable only when selecting Custom for Secure Boot Mode.

Feature	Option	Description
Secure Boot	[Disabled], [Enabled]	Secure Boot feature is Active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the System is in User mode. The mode change requires platform reset.
Secure Boot Mode	[Standard], [Custom]	Secure Boot mode options: Standard or Custom. In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.
Restore Factory Keys	[Yes], [No]	Force System to User Mode. Install factory default Secure Boot key databases.
Reset To Setup Mode	[Yes], [No]	Delete all Secure Boot key databases from NVRAM

Figure 71: BIOS Security Setup Menu –Secure Boot –Key Management

Aptio Setup – AMI				
Security				
Vendor Keys	Valid			
Factory Key Provision	[Disabled]			
> Restore Factory Keys				
> Reset To Setup Mode				
> Enroll Efi Image				
> Export Secure Boot variables				
Secure Boot variable	Size	Keys	Key Source	
> Platform Key (PK)	0	0	No Keys	
> Key Exchange Keys (KEK)	0	0	No Keys	
> Authorized Signatures (db)	0	0	No Keys	
> Forbidden Signatures (dbx)	1612	33	Modified	
> Authorized TimeStamps (dbt)	0	0	No Keys	
> OsRecovery Signatures (dbr)	0	0	No Keys	
→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit				
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Feature	Option	Description
Factory Key Provision	[Disabled], [Enabled]	Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode.
Reset Factory Keys	[Yes], [No]	Force System to User Mode. Install factory default Secure Boot key databases.
Reset to Setup Mode	[Yes], [No]	Delete all Secure Boot key databases from NVRAM.
Enroll Efi Image	Select a File system	Allow the image to run in Secure Boot mode. Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db).
Export Secure Boot variables	Select a File system	Save NVRAM content of Secure Boot variables to a file
Platform Key (PK)	[Details], [Export], [Update], [Delete]	Enroll Factory Defaults or load certificates from a file: 1. Public Key Certificate: (a) EFI_SIGNATURE_LIST (b) EFI_CERT_X509 (DER) (c) EFI_CERT_RSA2048 (bin) (d) EFI_CERT_SHAXXX
Key Exchange Keys (KEK)	[Details], [Export], [Update], [Append], [Delete]	2. Authenticated UEFI Variable 3. EFI PE / COFF Image (SHA256) Key Source: Factory, Modified, Mixed
Authorized Signatures (db)	[Details], [Export], [Update], [Append],	

Feature	Option	Description
	[Delete]	
Forbidden Signatures (dbx)	[Details], [Export], [Update], [Append], [Delete]	
Authorized TimeStamps (dbt)	[Update], [Append]	
OsRecovery Signatures (dbr)	[Update], [Append]	

12.2.4.1. Remember the password

It is highly recommended to keep a record of all passwords in a safe place. Forgotten passwords results in being locked out of the system.

If the system cannot be booted because the User Password or the Supervisor Password are not known, contact Kontron Support for further assistance.



HDD security passwords cannot be cleared using the above method.

12.2.5. Boot Setup Menu

The boot setup menu lists the for boot device priority order, that is generated dynamically.

Figure 72: BIOS Boot Setup Menu

Aptio Setup – AMI					
Main	Advanced	Chipset	Security	Boot	Save & Exit
Boot Configuration					
Setup Prompt Timeout		1			
Bootup NumLock State		[On]			
Quiet Boot		[Disabled]			
Fixed Boot Order Mode		[Enabled]			
Fast Boot		[Disabled]			
SATA Support [#]		[Last Boot SATA Devices Only]			
NVMe Support [#]		[Enabled]			
UFS Support [#]		[Enabled]			
VGA Support [#]		[EFI Driver]			
USB Support [#]		[Full Initial]			
PS2 Devices Support [#]		[Enabled]			
Network Stack Driver Support [#]		[Disabled]			
Redirection Support [#]		[Disabled]			
Boot mode select [*]		[UEFI]			
FIXED BOOT ORDER Priorities [*]					
Boot Option #1 [*]		[UEFI Hard Disk: Windows Boot Manager]			
Boot Option #2 [*]		[UEFI NVME]			
Boot Option #3 [*]		[UEFI CD/DVD]			
Boot Option #4 [*]		[UEFI SD]			
Boot Option #5 [*]		[UEFI USB Hard Disk]			
Boot Option #6 [*]		[UEFI USB CD/DVD]			→ ←: Select Screen
Boot Option #7 [*]		[UEFI USB Key]			↑ ↓: Select Item
Boot Option #8 [*]		[UEFI USB Floppy]			Enter: Select
Boot Option #9 [*]		[UEFI USB Lan]			+/-: Change Opt.
Boot Option #10 [*]		[UEFI Network]			F1: General Help
Boot Option #11 [*]		[UEFI AP:UEFI: Built-in EFI Shell]			F2: Previous Values
					F3: Optimized Defaults
					F4: Save & Exit
					ESC: Exit
> UEFI Hard Disk Drive BBS Priorities [*]					
> UEFI Application Boot Priorities [*]					
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* These items appear only when enabling Fixed Boot Order Mode.

These items appear only when enabling Fast Boot.

Feature	Option	Description
Setup Prompt Timeout	Value Input	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	[On], [Off]	Select the keyboard NumLock state [On]: The keys on the keypad will act as numeric keys. [Off]: The keys on the keypad will act as cursor keys.
Quiet Boot	[Disabled], [Enabled]	Enables or disables Quiet Boot option
Fixed Boot Order Mode	[Disabled], [Enabled]	If enabled then 'Fixed Order Boot Mode' is used, otherwise 'BCP boot order' (default). NOTE: if you changed this setting please immediately save & exit and re-enter setup to apply further changes on the boot settings!
Fast Boot	[Disabled], [Enabled]	Enables or disables boot with initialization of a minimal set of devices required to launch active boot option. Has no effect for BBS boot options.
SATA Support	[Last Boot SATA Devices Only], [All SATA Devices]	If Last Boot SATA Devices Only, only last boot SATA device will be available in Post. If All SATA Devices, all SATA devices will be available in OS and Post.
NVMe Support	[Disabled], [Enabled]	If Disabled, NVMe device will be skipped.
UFS Support	[Disabled], [Enabled]	If Disabled, UFS device will be skipped.
VGA Support	[Auto], [EFI Driver]	If Auto, only install Legacy OpRom with Legacy OS and logo would NOT be shown during post. Efi driver will still be installed with EFI OS.
USB Support	[Disabled], [Full Initial], [Partial Initial]	If Disabled, all USB devices will NOT be available until after OS boot. If Partial Initial, USB Mass Storage and specific USB port / device will NOT be available before OS boot. If Enabled, all USB devices will be available in OS and Post.
PS2 Devices Support	[Disabled], [Enabled]	If Disabled, PS2 devices will be skipped.
Network Stack Driver Support	[Disabled], [Enabled]	If Disabled, Network Stack Driver will be skipped.
Redirection Support	[Disabled], [Enabled]	If disabled, Redirection function will be disabled.
Boot mode select	[LEGACY], [UEFI], [DUAL]	Select boot mode LEGACY / UEFI
Boot Option #1..#11	[UEFI Hard Disk: Windows Boot Manager], [UEFI NVME], [UEFI CD/DVD], [UEFI SD], [UEFI USB Hard Disk], [UEFI USB CD/DVD], [UEFI USB Key],	Sets the system boot order

Feature	Option	Description
	[UEFI USB Floppy], [UEFI USB Lan], [UEFI Network], [UEFI AP:UEFI: Built-in EFI Shell], [Disabled]	

Figure 73: BIOS Boot Setup Menu –UEFI Hard Disk Drive BBS Priorities

Aptio Setup – AMI	
Boot	
Boot Option #1	[Windows Boot Manager]
→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit	
Version 2.22.1293 Copyright (C) 2025 AMI	

Feature	Option	Description
Boot Option #1	[Windows Boot Manager] [Disable]	Sets the system boot order

Figure 74: BIOS Boot Setup Menu –UEFI Application Boot Priorities

Aptio Setup – AMI	
Boot	
Boot Option #1	[UEFI: Built-in EFI Shell]
→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit	
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Feature	Option	Description
Boot Option #1	[UEFI: Built-in EFI Shell] [Disable]	Sets the system boot order

12.2.6. Save & Exit Setup Menu

The exit setup menu provides functions for handling changes made to the UEFI BIOS settings and the exiting of the setup program.

Figure 75: BIOS Save & Exit Setup Menu

Aptio Setup – AMI					
Main	Advanced	Chipset	Security	Boot	Save & Exit
Save Options Save Changes and Exit Discard Changes and Exit Save Changes and Reset Discard Changes and Reset Save Changes Discard Changes					
Default Options Restore Defaults Save as User Defaults Restore User Defaults Boot Override Windows Boot Manager UEFI: Built-in EFI Shell				→ ←: Select Screen ↑ ↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit	
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Feature	Description
Save Changes and Exit	Exit system setup after saving the changes.
Discard Changes and Exit	Exit system setup without saving any changes.
Save Changes and Reset	Reset the system after saving the changes.
Discard Changes and Reset	Reset system setup without saving any changes.
Save Changes	Save Changes done so far to any of the setup options.
Discard Changes	Discard Changes done so far to any of the setup options.
Restore Defaults	Restore / Load Default values for all the setup options.
Save as User Defaults	Save the changes done so far as User Defaults.
Restore User Defaults	Restore the User Defaults to all the setup options.
Windows Boot Manager	This group of functions includes a list of tokens, each of them corresponding to one device within the boot order. Select a drive to immediately boot that device regardless of the current boot order. If booting to EFI Shell this way, an exit from the shell returns to Setup.
UEFI: Built-in EFI Shell	

Appendix A: List of Acronyms



The following table does not contain the complete acronyms used in signal names, signal type definitions or similar. A description of the signals is included in the I/O Connector and Internal connector chapters within this user guide.

Table 19: List of Acronyms

2D	Two-Dimensional
3D	Three-Dimensional
AT	Advanced Technology
ATX	Advanced Technology eXtended
BGA	Ball Grid Array
BIOS	Basic Input / Output System
BSP	Board Support Package
CMOS	Complementary Metal Oxide Semiconductor
CPU	Central Processing Unit
DC	Direct Current
DDC	Display Data Channel
DIO	Digital Input / Output
DP	DisplayPort
ECC	Error-Correcting Code
EEE	Electrical and Electronic Equipment
EOS	Electrical OverStress
ESD	ElectroStatic Discharge
GbE	Gigabit Ethernet
HDD	Hard Disk Drive
HDMI	High Definition Multimedia Interface
LAN	Local Area Network
LED	Light Emitting Device
LVDS	Low-Voltage Differential Signaling
ME F/W	Management Engine Firmware
mPCIe	mini Peripheral Component Interconnect express
NGFF	Next Generation Form Factor
PC-AT	Personal Computer - Advanced Technology
PCB	Printed Circuit Board
PSU	Power Supply Unit
PVC	PolyViny Chloride
PWM	Pulse Width Modulation
RAM	Random Access Memory
ROM	Read-Only Memory

RTC	Real-Time Clock
SATA	Serial Advanced Technology Attachment
SD	Secure Digital memory card
SDP	Serial Download Protocol
SELV	Safety Extra-Low Voltage
SIM	Subscriber Identity Module
SMBus	System Management Bus
SoC	System on Chip
SO-DIMM	Small Outline Dual In-line Memory Module
SPD	Serial Presence Detect
SPI	Serial Peripheral Interface
TDP	Thermal Design Power
TPM	Trusted Platform Module
UEFI	Unified Extensible Firmware Interface
USB	Universal Serial Bus
UTP	Update Transfer Protocol
VGA	Video Graphics Array
WDT	WatchDog Timer
WEEE	Waste Electrical and Electronic Equipment



About Kontron

Kontron is a global leader in IoT / Embedded Computing Technology (ECT) and offers individual solutions in the areas of Internet of Things (IoT) and Industry 4.0 through a combined portfolio of hardware, software and services. With its standard and customized products based on highly reliable state-of-the-art technologies, Kontron provides secure and innovative applications for a wide variety of industries. As a result, customers benefit from accelerated time-to-market, lower total cost of ownership, extended product lifecycles and the best fully integrated applications.

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