Errata **1**

Manual: 27784
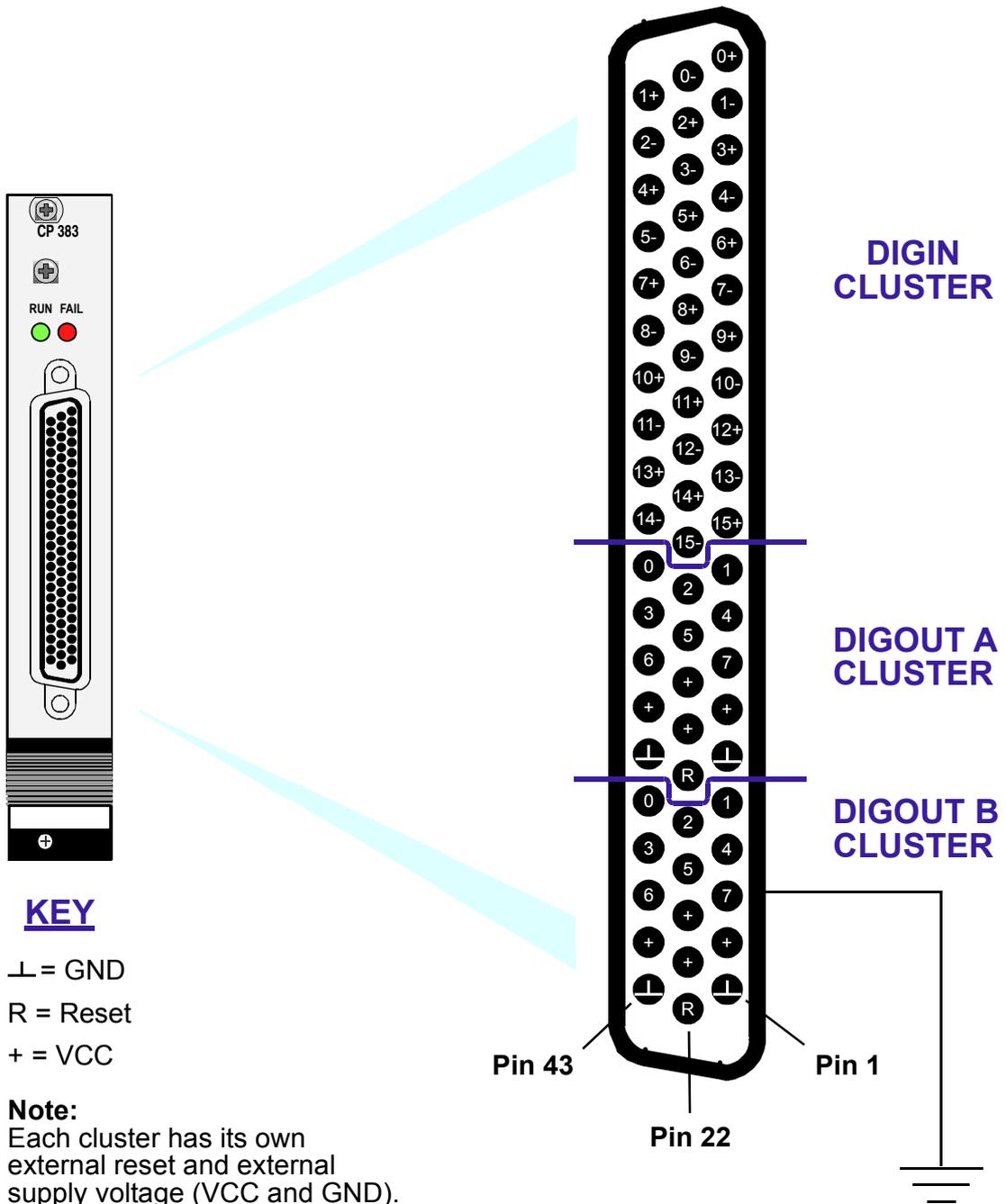
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1. Reference: Page 2 - 6, Chapter 2.7.1, Figure 2-2

The referenced information (figure) is superseded by the following replacement figure. The replacement figure revises pins 44 and 49.

Figure 2-2: Pin Layout of the Digital Input and Output Interface Connector CON2

KEY

⊥ = GND

R = Reset

+ = VCC

Note:
Each cluster has its own external reset and external supply voltage (VCC and GND). On the CP383, they are not connected with each other.

DIGIN CLUSTER

DIGOUT A CLUSTER

DIGOUT B CLUSTER

Pin 43

Pin 22

Pin 1

2. Reference: Page 4 - 10, Chapter 4.4.3, Table 4-4

The referenced information (table) is superseded by the following replacement table.

Table 4-4: Input Control Register

BITS	TYPE	DEFAULT	FUNCTION
31-8	R/W	0	Reserved
7	r/w	0	Input enable
6	r/w	0	Event detect enable
5	r/w	0	Latch mode enable
4	r/w	0	Pattern detect enable
3	r/w	0	Reserved
2-0	r/w	000	Debounce control deb [2 ... 0]