VX6124 6U OpenVPX Arm® Cortex®-A72 Computing Node



6U VPX 16 Core™ LX2160 Plug-In Card

- ▶ High End, Low power: 200k+ DMIPS, 30 W TDP processor
- > 16 Arm[®] Cortex[®]-A72 cores @ 2.2 GHz NXP LX2160A SOC
- **>** 16 128-bit NEON™ SIMD engines
- ▶ 40GBASE-KR4 data plane, 10GBASE-KR control plane port
- Developped in alignment with the SOSA™ Technical standard



VX6124 - 6U VPX 16 Arm® Core Computing Node

Kontron High End Arm® Architecture

Acknowledging the growing part of Arm® processors in the signal processing domain, Kontron extends its VPX portfolio of commercial-off-the-shelf (COTS) Computing Nodes segment with the VX6124, a 200k+ DMIPS processing blade featuring 40G Ethernet connectivity.

VX6124 is the 6U version of an architecture designed for long term programs and harsh environments, ideal for applications requiring outstanding bandwidth and digital security. This architecture can be used as a vehicle for 100G data plane system experiments (with Kontron VX6940 40G/100G switch for example).

The new architecture is built around the NXP QorlQ Layerscape LX2160A processor equipped with 16 GByte DDR4 memory. The air cooled version is thermally and mechanically designed to optimize the use of an XMC allowing for additionnal features like GPU option for addition of massive computing power or else FPGA option for extend of security features.

OpenVPX Interoperability and SOSA Alignement

Being aligned with the SOSA standard, the VX6124 board benefits in particular from increased interoperability, enabling the creation of complex systems made up of several CPU as well as switch boards.

According to VITA65 definition, the board is aligned with SLT6-PAY-4F1Q1H4U1T1S1S1TU2U2T1H-10.6.3-0 slot profile and MOD6-PAY-4F1Q1H4U1T1S1S1TU2U2T1H-12.6.3-2 module profile

QorlQ® Layerscape® LX2160A Communications Processor

The Layerscape LX2160A 'System on a Chip' (SoC) delivers the high-performance needed for compute-intensive networking applications. Equipped with sixteen Arm®v8 Cortex®-A72 CPU cores, 28 GHz SerDes technology and low FinFET power, this processor supports up to 100 Gbit/s Ethernet and the latest PCIe Gen4 technology. The wire rate I/O processor has 18 integrated MACs including dual 100 Gbit Ethernet ports and a 130 Gbit/s L2 switch. Every Arm®v8 Cortex®-A72 is associated with a 128-bit NEON™ SIMD engine.





LX2160A Key features

- ➤ 16 64-bit Arm® v8 Cortex®-A72 cores with 8 MByte L2 with ECC
- ▶ Dual DDR4 memory controllers with ECC, up to 3200 MT/s
- ➤ High speed Serdes I/O lanes with 35 GHz capability. Configurable for flexibility: 100G Ethernet and 1x PCIe x4 or SATA or mixed
- ➤ Switch function inside LX2160 enabling multiple ports Ethernet network
- **▶** Integranet DMA controllers
- Low sensitivity to cosmic radiation: suitable to space or avionics use cases.

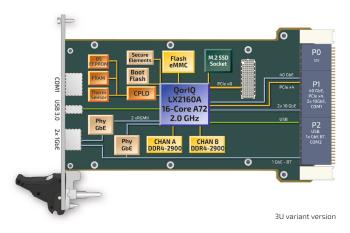
VX6124 Performance vs Power dissipation

The CPU subsystem fits a small power budget to enable efficient use of a mezzanine coprocessor. With the LX2160A processor speed set to 2.2 GHz, the VX6124 power budget does not exceed 30 Watts while offering the following performance:

- ➤ 253.4k DMIPS (7.9 DMIPS/MHz/A72core)
- **▶** 154.53k CoreMark (4.83 CM/MHz/A72core)

Other Form Factor

This Arm® high end architecture has been designed to be compatible with 3U VPX variants. Contact Kontron with your program specific requirements.



Designed to meet Airborne Environment

VX6124 is an air cooled unit designed to meet harsh environments like VITA 47 class EAC3. It can operate in Jet Cabin temperature environments of 0 $^{\circ}$ C/+55 $^{\circ}$ C. Built-In test capabilities enable effective monitoring. Shock and Vibration capability meet system qualification for D0160E, curve B12.

Software

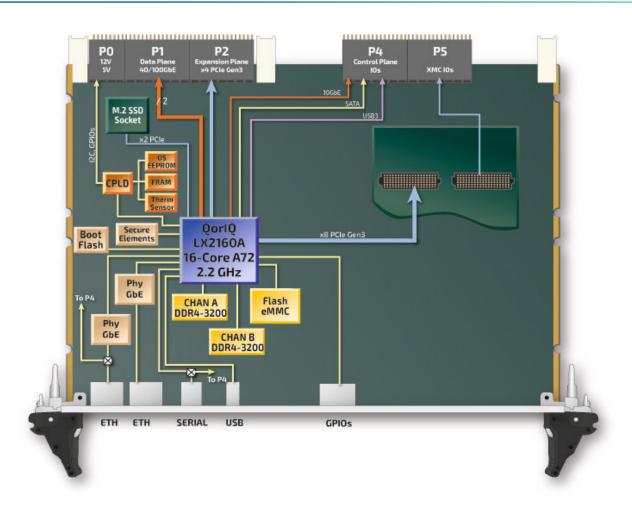
Kontron standard support includes U-Boot and Yocto Linux. Alternate boot device further secure field upgrades.

Other possible OSes include other Linux distributions (eg Fedora, Ubuntu, OpenWRT) via EFI Arm® boot as well as VxWorks™.

Digital Security

NXP Secure Boot and Arm® TrustZone are available. Kontron application code protection via AppProtect $^{\text{TM}}$ is also part of the VX6124 architecture. This enables a strict control of the deployment of critical applications at the unit level, with secure 'code in transit' and reverse engineering protection.

VX6124 Detailed Architecture



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FORM FACTOR		6U VPX, single slot, 5HP (fits 1.0 inch pitch backplanes and card cages)
MAIN PROCESSOR	SYSTEM ON CHIP	NXP QorlQ® Layerscape® LX2160A SOC 16 Arm® v8 Cortex®-A72 processor core running up to 2.2 GHz 30 Watts Power dissipation 28-nm silicon technology
ON-BOARD CONTROLLERS	ETHERNET PHY WATCHDOG SYSTEM CPLD	Two single port 10/100/1000BASE-T(X) Ethernet RGMII transceivers connected on front dual RJ45 connector PLD-based, timeout ranging from 4 ms to 510s, IRQ, Reset, dual-stages Power on/off control, reset control, local environmental control/monitoring, IZC interfaces to IZC bus IPMB A/B (rear PO), LEDs control, user and system GPIOs, internal registers that allow system management
MEMORY	SYSTEM MEMORY OS STORAGE FLASH (UBOOT) EEPROM F-RAM	16 GByte dual channel DDR4 SDRAM running at 3200 MT/s, ECC, soldered 32 GByte MLC 5.1 eMMC device 2x 512 Mb serial NOR flash, with recovery image and UBoot settings One serial 256 Kbit EEPROM dedicated to system data One serial 256 Kbit EEPROM dedicated to application data 1-Mbit, non-volatile, FRAM dedicated to the backup of critical data when the board is powered off
FRONT PANEL CONNECTIVITY	ETHERNET USB 2 AND 3 SERIAL LINE LEDS RESET	2 x 10/100/1000BASE-T(X) Ethernet interface on dual RJ45 connector 1 USB 2.0 and USB3.0 interface on USB type A upright connector 1 TIA-232 serial line with handshaking or TIA-42Z/485 or 2 TIA-232 simplified on IEEE1394 type connector, depending on build option 5 LEDs reporting the board CPU health status and activity Reset push button
ONBOARD CONNECTIVITY	M.2 SOCKET XMC SLOT	Bottom M.2 socket for SSD module. Supported size: Type M, 22 mm x 42 mm. VITA 61 XMC2.0: x8 PCIe Gen3 direct link to SOC, 12V VPWR setting VITA 46.9 XMC I/O routing: X24s + X8d-X12d
BACKPLANE CONNECTIVITY	VPX INTERFACE PO SUPERVISORY FUNCTIONS PO POWER SUPPLY P1 P2 P4 P5	SLT6-PAY-4F1Q1H4U1T1S1S1TU2U2T1H-10.6.3-0 slot profile MOD6-PAY-4F1Q1H4U1T1S1S1TU2U2T1H-10.6.3-2 module profile Non Maskable RESET NVMRO, Master SMBus and Master/Slave SMBus interfaces for system management, temperature and voltage sensors on the board PCle optional use of common reference clock feature P0: VS1=12V, 3.3V_AUX, -12V_AUX for XMC slot VS2 and VS3=5V not used Data Plane: 2x 40/100GBASE-KR4 1x 10GBASE-KR Maintenance port, GPlOs Expansion Plane: x4 PCle Gen2 Serial Lines, GPlOs Control Planes: 1x 10GBASE-KR + 1x 1000BASE-T IOs: 1x SATAIII, 1x USB2.0, 1x USB3.0, serial line XMC IOs: X24s + X8d-X12d
SOFTWARE SUPPORT		Uboot, Linux available now. Ask for: Windows, VxWorks
MECHANICAL SIZE		$6\text{U}\times160$ mm, Slot pitch: 1.00 inch according VITA 48.0 and VITA 48.1 Type 2

Ordering Information

ARTICLE	PART NO.	DESCRIPTION
VX6124-SAFF-1020000	1070-9990	6U Single slot 5HP (1") VPX SBC -Air-Cooled 'SA' (0 °C to 55 °C) - LX2160A sixteen Arm® A72 2.0 GHz QorlQ LayerScape Processor - 16GByte soldered SDRAM with ECC - Soldered 32GByte eMMC Flash - M2 SSD bottom slot, XMC 2.0 slot - SLT6-PAY-4F1Q1H4U1T1S1S1TU2U2T1H-10.6.3-0 slot profile - No Secure element No PBIT - No coating

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About Kontron

Kontron is a global leader in IoT/Embedded Computing Technology (ECT) and offers individual solutions in the areas of Internet of Things (IoT) and Industry 4.0 through a combined portfolio of hardware, software and services. With its standard and customized products based on highly reliable state-of-the-art technologies, Kontron provides secure and innovative applications for a wide variety of industries. As a result, customers benefit from accelerated time-to-market, lower total cost of ownership, extended product lifecycles and the best fully integrated applications.

For more information, please visit: www.kontron.com

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