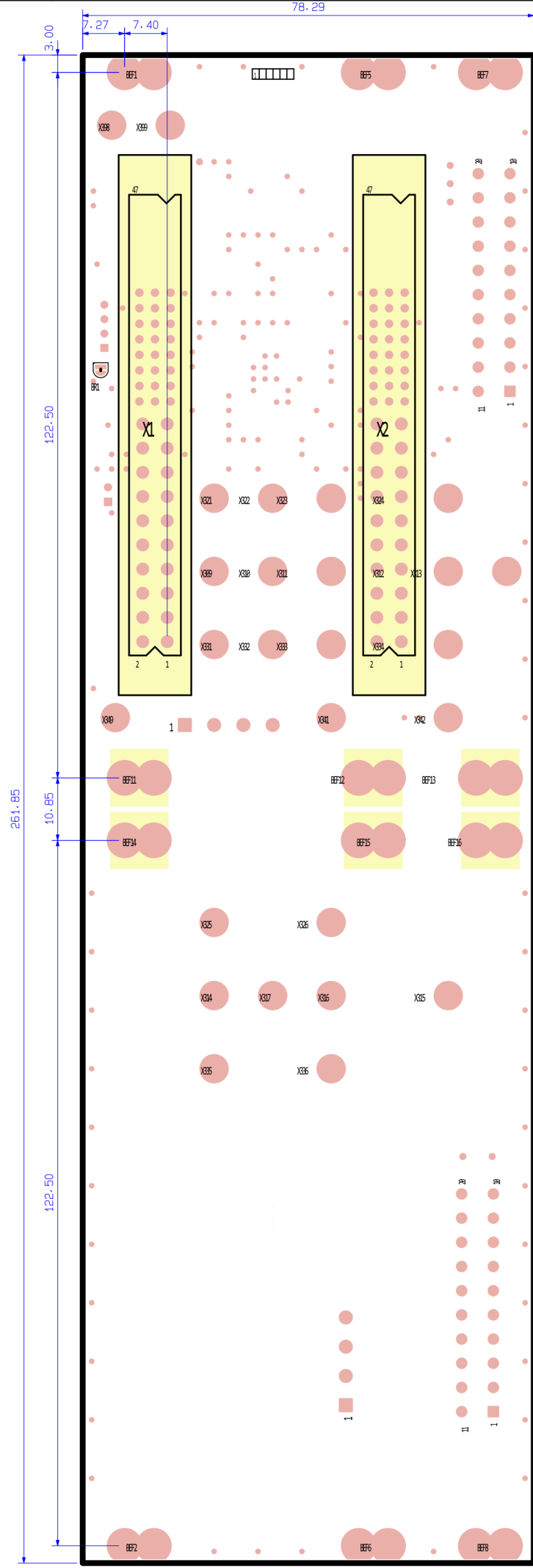


[illegible]

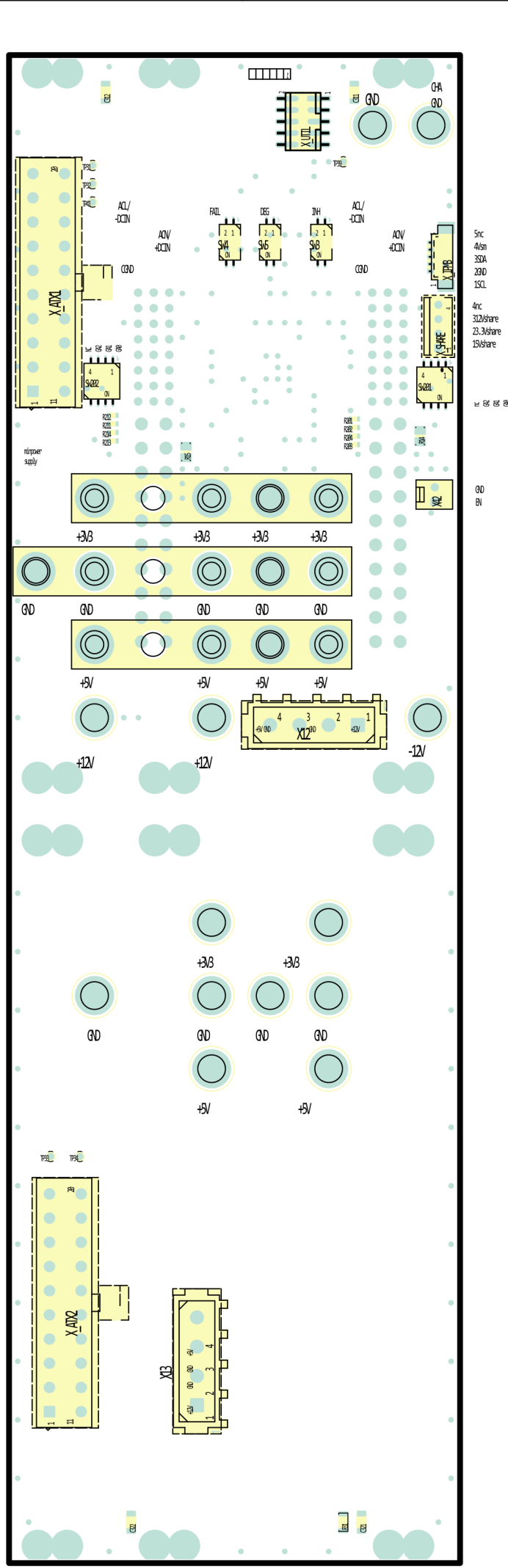
Xilinx Prosignment			
Ptn	Signal	Ptn	Signal
1	PPH	2	RD
3	DP	4	+3.3V Sense
5	+3.3V	6	RD Sense +3.3V
7	eV	8	+5V Sense
9	RD	10	RD Sense eV

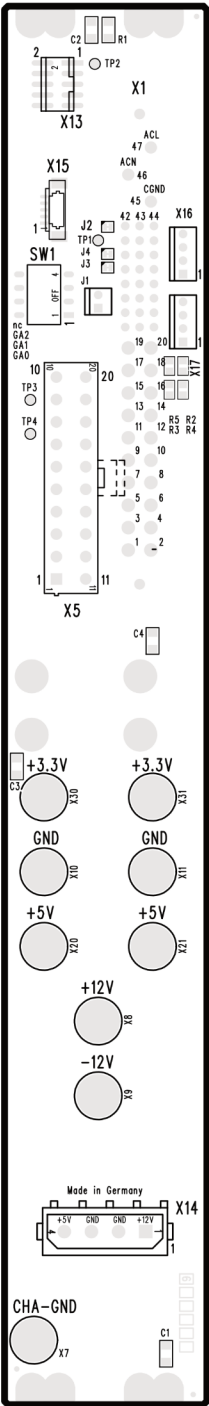
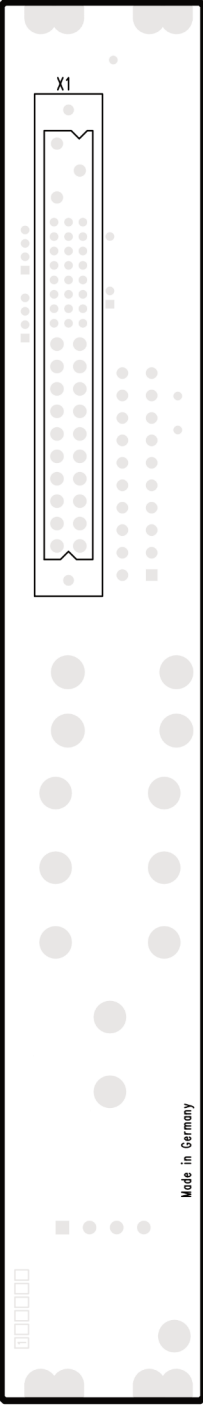
Xilinx Prosignment			
Ptn	Signal	Ptn	Signal
1	RD	2	RD Sense eV
3	eV	4	eV Sense
5	ACRD-	6	ACRD-
7	SSRD-	8	SSRD-
9	SSRD+	10	SSRD+
11	+3.3V	12	+3.3V Sense
13	RD	14	RD Sense +3.3V

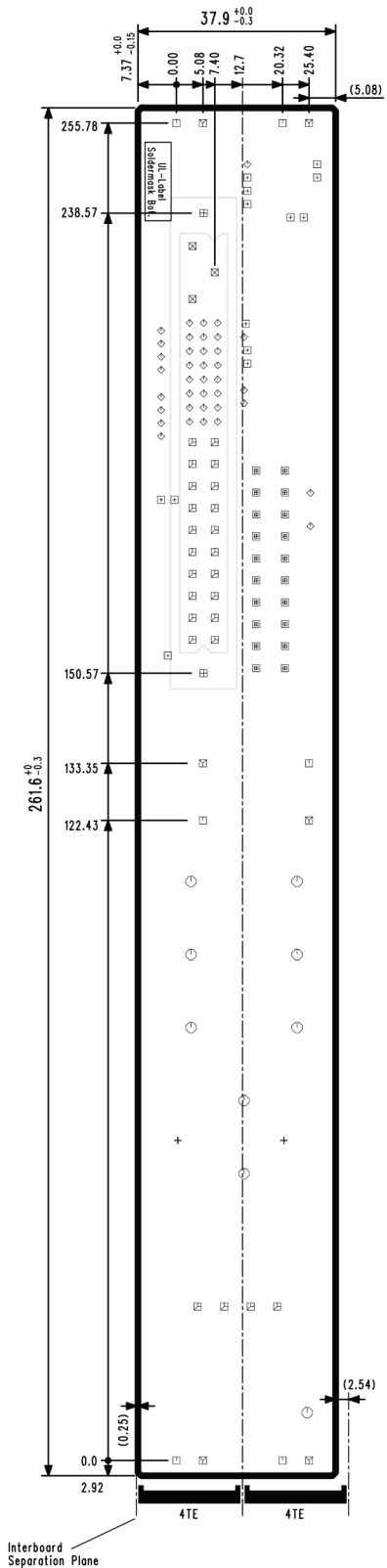
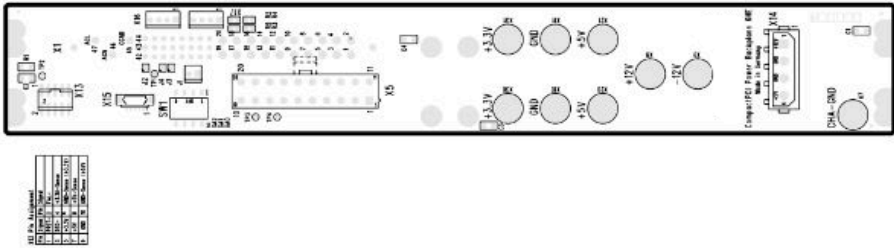
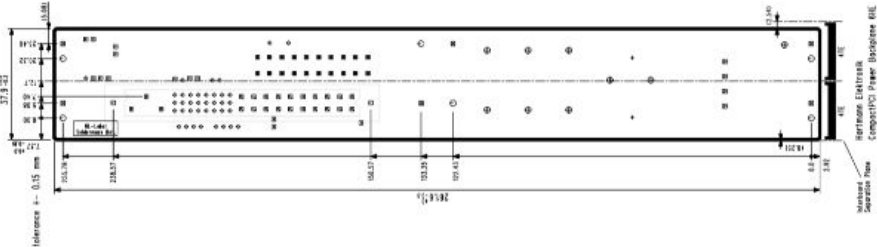
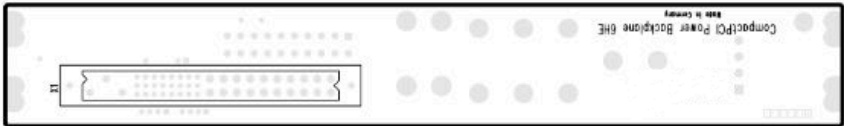
ADPwr

Connector

PwrB	PwrB
+12V	+5V
TPxx	+5V
RD-	TPxx
RD	RD
+5V	RD
RD	RD
+5V	TH+
RD	RD
+3.3V	-12V
PwrL	PwrL
+3.3V	+3.3V

[illegible]

[illegible][illegible]



All rights reserved

Rev	Description	Day	Name	2003	Date	Name	Author	28.05.	Homann	Check	28.05.	Brosowski	DIN A3	M 1:1	Order No.:	36L 1 000 014	Sheet	3	of 3
1																			
2																			
3																			
4																			
5																			
6																			

1

2

3

4

5

6